

STAFF SELF APPRAISAL REPORT**2018-2019****KSSEM**

Field	Data	Score
Name	MANJULA B G	
Present Address, Mob. No., e-mail id.	#10, "Sri Vittala Chaithanya Nilaya , J.P.Nagar 7 th Phase, 7 th Main, Shreyas Colony, Bangalore, Karnataka State 560078 Mobile No: 8884123708 E-mail id: manjula.b.g@kssem.edu.in	
Age and Date of Birth	Forty-three and 15-07-1976	
Qualification	M.Tech	
Designation and Department	Associate Professor, Electrical & Electronics Engineering Department	
Teaching Experience (After PG)	7.5 years	---
Other Experience (If any)	1. Lecturer in Reddy Jana Sangha Polytechnic for 4 Years and 1 Months. 2. Trainee Engineer in Therelek Engineer's Pvt Ltd, Bangalore for 1 Year and 7 Months. 3. Guest Lecturer in Gurunanak Dev Government Polytechnic (Govt. of Delhi and NCR) New Delhi-5 months.	
List of Subjects Taught till date and percentage pass (use separate sheet if necessary)	Attached Annexure 1	
Subjects taught in the Assessment Year and percentage pass (10marks for each x Percentage)	1. Power System Operation & Control (15EE81) - 100 % 2. Power System Operation & Control (10EE82) - 100 % 3. Basic Electrical engineering (17ELE25) - Waiting for results 4. Electrical Machines Laboratory II (17EEL48) - Waiting for results 5. Utilization of Electrical Power (15EE742)-100 % 6. Basic Electrical engineering - 86% 7. Electrical Machines Laboratory I (17EEL37) - 100 % 8. Basic Electrical Engineering Laboratory (18ELE17) - 100 % 9. Electrical Power Utilization(10EE72)- 100 %	40 / 40

Details of UG Projects Guided (5 marks/ project guided)	Design and Development of transmission line protection unit using PLC.	5/10
Details of PG Projects Guided (5 marks/ project guided)	Not Applicable	/10
Additional Inputs given in the class in addition to the syllabus (Give proof and justification) (If applicable)	Model display	5 / 5
Guest / Invited Lectures arranged (2marks /lecture) Max 5 marks.	A Guest lecture was arranged for Electrical and Electronics Engineering students on 29/04/2018. Mr. Arun Kumar, Centre Manager from Edu CADD delivered a talk on "Application and Opportunities of CADD in Electrical Engineering"	2 / 5
Details of Industrial Visits arranged. (2marks/visit) Max 5 marks.	Indian Institute of Science (open day) visit on 23-3-2019	2 / 5
Number of FDPs attended since joining service (Attach Separate List)	Attached Annexure II	--
Details of students mentored during current assessment year.	10	--
Details of Participation in VTU Bodies (2 Marks)	-	/ 2
Details on Examination related Activity (2marks each)	1. Practical Exams: Appointed as internal and External lab examiner 2. Conduction of Theory exams: RS and DCS duty in 2018 theory exams 3. Paper Setting: Power System I(EE61) Dayanand Sagar College of Engineering May-June 2019 4. Evaluation: 10EE82 and 18ELE13	8/8
List of FDPs attended during the Assessment year (5 marks each) (Attach Certificate copies)	Half a day Faculty Development Programme on "Learn 2 Learn" held on 19-07-2019 organized by department of Computer Science in association with Board for IT Education Standards at KSSEM.	5 / 10
Financial Assistance received during current year for attending FDPs	NIL	--
Status of Ph.D. [Attach proof for each stage] (This can be claimed only once during a life time after the PhD is awarded)	1. Appeared for Course work exams (1 mark): Taken only two subjects 2. Applied for registration formalities (1 mark) 3. Identified Guide/Research Centre and preparing research Proposal (1mark.)	2 / 10

[Attach proof for every claim]	4. Not thought of pursuing Ph.D. (zero)	
Research Publications: (5 marks each) [Attach copies of Title Page]	<ol style="list-style-type: none"> 1. "Design and Development of Data Acquisition and Controlling Unit for Power Generation" International Journal of Engineering Research, volume 9, issue 7, July 2019, pp 29-32, Doi 10.9790/9622-0907052932 ISSN: 2248 – 9622 2. "Design and Implementation of Rain Water, Humidity and Temperature Detector Alarm Unit for Green House Irrigation System" SSRG International Journal of Electrical and Electronics Engineering (SSRG-IJEEE), Page 19–22, Volume 4 Issue 5, May 2017 ISSN: 2348-8379. 3. "Design and Development of a Capacitive Power Transfer for Contactless Charging of Low Power Devices" SSRG International Journal of Electrical and Electronics Engineering, Volume 3 Issue 5, Page 41–44, May 2016, ISSN: 2348-8379. 4. "Contingency Ranking in Power Systems Employing Fuzzy Based Analysis" International journal of Electrical Engineering and Embedded systems, Vol.4, Jan-June 2012, Page No.1-7, ISSN:0975-4830 	10 / 10
Seminars / Workshops / Conferences attended (5 Marks each) [Attach Certificate Copies]	<ol style="list-style-type: none"> 1. One Week Workshop on "Research Methodology" held from 30th July to 4th August 2018 at VTU Human Resource Development Centre for Post Graduate Studies, Muddenahalli, Chikkaballapur District. 2. One day workshop on "New Approach to the Revised Assessment & Accreditation of NAAC" held at Global Academy of Technology on January 4th, 2019 	10/10
Financial Assistance received during current year	NIL	--
Registered as Research Guide (Reasons for not registering)	-	
No. of Research Scholars registered with details	-	/5
Details of Patents Applied for (If any)	-	/5
Academic Programs organized and supported during current year. (FDP/Workshop/Seminar / Conference)	Workshop on "Embedded System for IOT Applications" 25th & 26th March 2019 at KSSEM	5/5

Details of programs attended for skill development like MOOCs, MOODLES and others	-	/5
Details of Utilization of NPTEL and other Online materials for augmenting own lectures.	<ol style="list-style-type: none"> 1. Power System Operation and Control: http://nptel.ac.in/courses/108104052/3 2. Basic Electrical Engineering: http://nptel.ac.in/courses/108108076/ 3. Electrical power utilization: www.nptelvideos.in/2012/11/illumination-engineering.html 	5/5
Details of Project Proposal submitted during the current year. (At least one)	Preparation of project proposal for the projects under PMKVY (Pradhan Mantri Koushal Vikas Yojana) submitted to AICTE under power sector for academic year 2018-19.	5/5
Details of Project Funds Received.	-	/5
Consultancy Revenue Generated	-	/5
Details of Participation in cultural events during the current year	<ol style="list-style-type: none"> 1. In charge of Aarohana 2019 Stage Committee. 2. Organizing Voice of KSSEM, Sketching and Dumb charades for Staff and Students. 3. Appointed as a Judge for Staff Rangoli competition in Aarohana 2019 	5/5
Additional Responsibilities in the Department/ College Example: Head, Coordinator etc.	<ol style="list-style-type: none"> 1. In-charge Head of Electrical and Electronics Engineering Department from 1-2-2019 to 30-5-2019. 2. NAAC 2 Coordinator, News Letter Coordinator 	10
Details of Live Membership for Professional Bodies (IEEE CSI SEA ISTE)	Life member of Indian Society for Technical Education. Membership No. LM94795	5/5
Graduation Day Responsibilities. (If any) Please mention your role.	<ol style="list-style-type: none"> 1. Member of Stage organizing committee 2. Preparation of prize list required for the different events like student data, best outgoing student from department, best student from each semester and so on for the department. 	5/5
TOTAL		129/190

Date: 17-7-2019

[Handwritten Signature]
17/7/19

Associate Professor
Head of the Department
Department of Electrical & Electronics Engineering
KS School of Engineering and Management
Bengaluru-560 109

[Handwritten Signature]
Signature of faculty

[Handwritten Signature]
Dr. K. RAMA NARASIMHA
Principal/Director
KS School of Engineering and Management
Bengaluru - 560 109

Annexure I

List of Subjects Handled from 2012-13 August to Till Date

Sl.No	Semester/Year	Class	Subject	Theory/Lab	Over all Class Result (%)
1	III /2012-13	EEE	Electrical & Electronic Measurements and Instrumentation Engg	Theory	95.45
2	V /2012-13	EEE	DC and Synchronous Machines	Theory	100
3	V /2012-13	EEE	Transformers and Induction Machine Laboratory	Lab	100
4	VI/ 2012-13	EEE	Switch Gear and Protection	Theory	100
5	IV /2012-13	EEE	Transformer and Induction motor	Theory	72.72
6	VI /2012-13	EEE	DC and Synchronous Machines	Lab	100
7	V /2013-14	EEE	DC and Synchronous Machines	Theory	83.33
8	VII /2013-14	EEE	Electrical Power Utilization	Theory	100
9	II /2013-14	ECE	Basic Electrical Engg.	Theory	96.72
10	VIII	EEE	Power System Operation and	Theory	96.55

	/2015-16		Control		
23	VI /2015-16	EEE	Control System Lab	Lab	100
24	I /2016-17	CSE	Basic Electrical Engg.	Theory	67
25	V / 2016-17	EEE	DC and Synchronous Machines	Theory	97
26	V / 2016-17	EEE	Transformers and Induction Machines Lab	Lab	100
27	II /2016-17	CIVIL	Basic Electrical Engg.	Theory	51
28	VIII /2016-17	EEE	Power System Operation and Control	Theory	91.17
29	VI /2016-17	EEE	DC and Synchronous Machines Lab	Lab	100
30	I/ 2017-18	CSE	Basic Electrical Engg.	Theory	73%
31	VII /2017-18	EEE	Electrical Power Utilization	Theory	100
32	III / 2017-18	EEE	Electrical Machines Lab 1	Lab	97
33	V /2017-18	EEE	Transformers and Induction Machines Lab (parallel course)	Lab	100
34	VIII	EEE	Power System Operation and	Theory	

	/2017-18		Control		100
35	II /2017-18	EC	Basic Electrical Engg.	Theory	80%
36	IV /2017-18	EEE	OP AMP and LIC Lab	Lab	97
37	I/ 2018-19	CSE	Basic Electrical Engg.	Theory	86
38	VII /2017-18	EEE	Utilization of Electrical Power	Theory	100
39	III / 2018-19	EEE	Electrical Machines Lab I	Lab	100
40	VII /2018-19	EEE	Electrical Power Utilization (Exit scheme)	Theory	100
41	I/ 2018-19	CSE	Basic Electrical Engg.	Lab	100
42	II/ 2018-19	ECE	Basic Electrical Engg.	Theory	Waiting for results
43	VIII/ 2018-19	EEE	Power System Operation and Control (15EE81)	Theory	100
44	VIII/ 2018-19	EEE	Power System Operation and Control (10EE82)	Theory	100
45	IV/ 2018-19	EEE	Electrical Machines Lab II	Lab	Waiting for result

ANNEXURE II

FDP, Conferences, Workshops & Seminars Attended and Organized

- Participated in One day Workshop on “New Approach to the Revised Assessment and Accreditation of NAAC” on 4th January 2019 held at Global Academy of Technology, Bangalore.
- Participated in One Week Workshop on “Research Methodology” held between 30th July to 4th August 2018 at VTU Human Resource Development Centre for Post Graduate Studies, Muddenahalli, Chikkaballapur District.
- Participated in TEQIP -III Sponsored One Week Workshop on ‘Recent Trends in Power System Operation and Control’ held during 11th to 15th June 2018 in the National Institute of Technology Surathkal Mangalore.
- Participated in Two days Faculty Development Program on Research Proposal Preparation towards Ph.D. Admission Programmes” held on 24th and 25th June 2016 at KSSEM Bangalore.
- Participated in Two days workshop on “Review of Latest Trends in Transformer Design, Manufacture and CAED” held on 28th and 29th March 2016 at KSSEM Bangalore.
- Participated in the FDP on “Outcome Based Education and Bloom’s Taxonomy” held during 4th and 5th December 2015 at KSSEM Bangalore.
- Participated in the FDP on “Training the Trainer Workshop on Intellectual Property Rights Significance for Academia” held at Bangalore on 31st July 2015
- Organized one day national conference on “Horizons in Power Engineering “(NCHPE-15) by the department of Electrical and Electronics Engineering on 29th April 2015 held at KSSEM Bangalore.
- Participated in the “Young Researchers Meet” organized by IEEE PES Bangalore Chapter on 6th December 2014 at Bangalore
- Participated in the Two-day workshop on “Lab View” conducted by Cranes Software International Ltd and National Instruments on 4th and 5th September 2014
- Participated in one day workshop on “Research Methodology” held at KSSEM on January 30, 2014
- Attended three days Training Program on “MI-Power Software” held at KSSEM Bangalore from 10th to 12th September 2013.
- Organized three Days VTU-VGST Sponsored FDP on “Recent Advances in Nano Devices and Sensor Technology” held at KSSEM Bangalore from 8th to 11th July 2013.
- Attended Three-day workshop on “National Level Workshop on Computer Aided Electrical Drawing” held at HKBK College of Engineering, Bangalore from 7th to 9th January 2013.
- Participated in Two days workshop on “Recent Trends in Communication Computing and Signal Processing” held at Guru Tegh Bahadur Institute of Technology, New Delhi on March 30-31, 2012.
- Participated in one day workshop on “Recent Trend and Advances in Power Electronics and Drives” held at Guru Tegh Bahadur Institute of Technology, New Delhi on March 17, 2012.

CONTINGENCY RANKING IN POWER SYSTEMS EMPLOYING FUZZY BASED ANALYSIS

MANJULA B. G.¹, SUMA A. P.², A. D. KULKARNI², NARENDRA KUMAR¹ AND
T. ANANTHAPADMANABHA²

¹Dept of Electrical Engineering, DTU, New Delhi, India

²Dept of Electrical Engineering, The National Institute of Engineering, Mysore, India

Abstract: *In deregulated operating regime power system security is an issue that needs due consideration from researchers. Real power & voltage contingency ranking is an integral part of security assessment. The objective of contingency screening & ranking is to quickly & accurately shortlist critical contingencies from a large list of credible contingencies & rank them according to their severity for further rigorous analysis. A performance index (PI) is computed for each single line contingency using both conventional & Fuzzy based approach. To obtain the magnitudes of various parameters, a computer aided power system study software package which employ iterative methods are used. This paper presents an approach using fuzzy logic to evaluate the degree of severity of the conventional contingency & to eliminate the masking effect in the technique.*

Index Items: *System Security, Real power, Voltage, Performance index (PI), Contingency ranking, and Fuzzy logic (FL) approach, SCADA*

I. INTRODUCTION

The effect of the line outage when rest of system is stable is called contingency study. The study of contingency is an essential activity in planning operation & control of power systems. Contingency analysis allows systems to be operated defensively. The operator cannot take action fast enough when many of the problems that occur on a power system that causes serious trouble within fraction of time period leading to cascading failures. Because of this aspect of system operation, modern operations computers & SCADA systems are equipped with contingency analysis programs that model possible system troubles before they arise.

The outage or change in the independent parameters of the power systems gives rises to transient phenomena in the electrical and electromechanical states of those power systems

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[1]. The main thrust of contingency studies carried out in power system control centers is to determine the steady state effects of outages [2]. Large power systems require the analysis of all the credible contingency within a very short time so as to exercise the control in the short time available for corrective action [3]. In performance index method with high exponent, the resultant performance index value will depend heavily on loading of the particular line which is loaded closest to its limit, i.e. 90% to 95% of the rated capacity, other lines which are less heavily loaded i.e. 80% to 85% of the rated capacity though of large in number, will have relatively small weightage on the performance index value [4]. Ranking all possible contingencies based on their impact on the system voltage profile will help the operators in choosing the most suitable remedial actions before the system moves toward voltage collapse [15]. In [16], surveying possible contingencies with ranking according to line

Design and Development of a Capacitive Power Transfer for Contactless Charging of Low Power Devices

Bhuvaneshwari S^{*1}, Dakshayani S^{*2}, Rakshak B R^{*3}, Manjula.B.G. ^{*4}

^{*1, *2, *3} Student, Electrical & Electronics Engineering, K.S.S.E.M. Bengaluru, Karnataka State, India.

^{*4} Assistant Professor, Electrical & Electronics Engineering, K.S.S.E.M. Bengaluru, Karnataka state, India

Abstract —In this technically advanced era, technology is the cornerstone of civilization. The importance of technology brings into focus the value of charging technologies. The scope of the industry is moving towards Wireless Charging as this allows for easier charging without the use of wires and is extremely convenient. Wireless chargers that exist currently are inductively coupled. Inductive coupling tends to cause electromagnetic disruption in most of the devices that is sensitive to electromagnetic interference. This design which is based on capacitive coupling overcomes the disadvantage of any interference with other devices as it is based on Electrostatic Coupling. This design is able to charge a low device through contactless charging efficiently.

The designed wireless charger unit was simulated in Proteus & Professional Suite. A transformer is used to step down the supply voltage to the requisite value. The H-Bridge circuit is used to convert dc to ac on the Transmitter side and the Rectifier circuit is used to convert ac back to dc in the Receiver side of the device. An LED which is a low powered device is connected to the receiver side of the circuit as a load. Power is transferred to the circuit via the plates which are separated by a dielectric medium.

A wireless charger was built, however, the size and the weight are two very important criteria in the design of any charger and these factors are affected by the effective plate size of the coupling capacitors and must be taken into consideration for the design of a charger.

Keywords: Capacitive Coupling, Proteus & Microcontroller, LED.

I. INTRODUCTION

The world has advanced to a stage where technology is an extremely vital part of our daily lives. As the dependency on technology increased, the need for

more efficient, more compact, smaller and lighter devices are required. The technology used in present charging systems for these devices are outdated and inept. The commonly used wired chargers lack flexibility in movement and lack efficiency in charging of multiple devices simultaneously. Wireless Power Transfer is gaining increasing attention for charging of low powered devices like smart phones, cameras and laptops. Present Wireless Chargers are based on an inductive interface between the transmitter and receiver. Both the transmitter and receiver are fitted with electrical coils. When brought close to each other, power is transferred from the transmitter to the receiver. An alternate approach using a capacitive interface rather than inductive interface is used to deliver the power. Electrostatic field is confined between the plates in this approach, dispelling the need for flux guiding and shielding components used in inductively coupled systems which not only add bulk but also increase the overall cost of said systems.

Generally, a transformer which consists of two coils is used for transfer power keeping in mind the need to achieve isolation in circuits. Similarly, capacitors can be used for isolation, but the method of common-mode rejection is different. These signals do not induce flux in the transformer core. However, practically, finite capacitive coupling from the primary to the secondary leads to common mode feed through. This is not a feasible outcome. It is because the signals are limited to low frequencies and their harmonics. The impedance of capacitors at low frequencies is high, limiting the common mode current that passes through. Thus capacitances can also be used to isolation, and power can be delivered at much higher frequencies.

Design and Implementation of Rain Water, Humidity and Temperature Detector Alarm Unit for Green House Irrigation System

Jyothi Prakash^{#1}, Bimbika R Patel^{#2}, Birendra Kumar Bhagat^{#3}, Nikhil Bhaskar^{#4}, Manjula B.G^{#5}
^{#1, #2, #3, #4} Student, Electrical & Electronics Engineering, K.S.S.E.M, Bengaluru, Karnataka State, India,

^{#4} Associate Professor, Electrical & Electronics Engineering, K.S.S.E.M, Bengaluru, Karnataka state, India

Abstract —The most important factor of quality, productivity of plant growth varies as temperature, humidity and the level of the carbon dioxide. Continuous monitor of these environmental variables provides information regarding factors which affect the growth of plant grown inside green house and method to manage productiveness. The optimal greenhouse adjustment can enable us improve productivity and to achieve remarkable energy savings. Our design is able to obtain greenhouse irrigation control by using the developed Rain water, Humidity and Temperature Detector-Alarm Unit.

The designed alarm unit was simulated in Proteus 8 Professional Suite. The circuit consisting microcontroller, LCD, transformer and relay is built on a wooden board. The glass Green house is built for green house irrigation purpose. The required supply is provided to all components used in the circuit. Different sensors are used to control the different parameters in the green house and to create a suitable environment for the Green house irrigation.

Keywords: Proteus 8, Microcontroller, LED, Rain sensor, Humidity sensor, LM35.

I. INTRODUCTION

A green house is a structure with walls and roof made chiefly of transparent material, such as glass, in which plants requiring regulated climatic conditions are grown. These structures range in size from small sheds to industrial-sized buildings. The interior of a greenhouse exposed to sunlight becomes significantly warmer than the external ambient temperature, protecting its contents in cold weather. Greenhouse protects crops from too much heat or cold, shield plants from dust storms and blizzards, and help to keep out pests. The greenhouse allows certain crops to be grown throughout the year, greenhouses are increasingly important in the food supply of high latitude countries. Pests and diseases, and extremes of

the heat and humidity, have to be controlled, and irrigation is necessary to provide water. Significant inputs of the heat and light may be required, particularly in the winter season for the production of warm weather vegetables. Because the temperature and humidity of greenhouses must be constantly monitored to ensure optimal conditions, a WSN can be used to gather data remotely. The data are transmitted to a central location and used to control heating, cooling, and irrigation systems.

The system itself was usually simple without opportunities to control locally heating, lights, ventilation or some other activity, which was affecting the greenhouse interior climate. This all has changed in the modern greenhouses. The typical size of the greenhouse itself is much bigger what it was before, and the greenhouse facilities provide several options to make local adjustments to the lights, ventilation, heating and other greenhouse support systems. However, more measurement data is also needed to make this kind of automation system work effectively. Increased number of measurement points should not dramatically increase the automation system cost. It should also be possible to easily change the location of the measurement points according to the particular needs, which depend on the specific plant, on the possible changes in the external weather or greenhouse structure and on the plant placement in the greenhouse.

II. LITERATURE REVIEW

Rainwater harvesting is a simple technique of collecting and reserving rain water. Either, one can store it in tanks or can use it to recharge ground water depending upon the situation. It is ideal for areas where there is inadequate ground water supply or surface resources. The system helps in utilizing the primary source of water and prevents the run off from going into sewer or storm drains, thereby reducing the load on treatment plants. Rainwater harvesting

VTU / MH / RM5 / JULY - AUGUST - 2018 / ..2..5..



VISVESVARAYA TECHNOLOGICAL UNIVERSITY, BELAGAVI

VTU HUMAN RESOURCE DEVELOPMENT CENTRE (VTU-HRDC)

VTU Centre for Post Graduate Studies, Bangalore Region
Muddenahalli, Chikkaballapur (Dist) - 562101

One Week Workshop

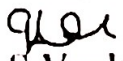
On


“ RESEARCH METHODOLOGY ”

30th July to 4th August 2018

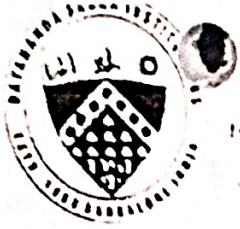
Certificate

*This to certify that Mr/Ms.....MANJULA.....B.S.....of.....KSEM.....BENGALURU.....
has participated and Successfully completed One Week Workshop On “ Research Methodology ”
from 30th July to 4th August 2018 Organized by VTU Human Resource Development Centre (VTU-HRDC)
Centre for PG Studies, Muddenahalli, Chickaballapur (Dist).*


Dr. G S Venkatesh
Convener
VTU, CPGSB, Muddenahalli.


Dr. H N Jagannatha Reddy
Registrar
VTU, Belagavi.


Dr. Karisiddappa
Vice Chancellor
VTU, Belagavi.



DAYANANDA SAGAR COLLEGE OF ENGINEERING

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Dr Kiran S
Controller of Examinations

email: coe-dscc@dayanandasagar.edu
Ph: 080-42117117

DSCE/EXAM/ / 2018-2019

Date: 21/03/2019

Strictly Confidential

To
Prof. Manjula
Designation: Associate
Department of: EEE
College: KSSEM

Respected Sir/ Madam,

Sub: Appointment as Paper Setter for May-June 2019 UG Examinatllons

It is my pleasure to inform that you are appointed as paper setter for Undergraduate Engineering Examinations of May-June 2019. I request you to kindly set the paper for the Course mentioned below and submit the same by 15th April 2019.

Level	UG
Program	BE. (EEE)
Semester	6 th
Course title	Power Systems -I
Course code	EE61

Guidellnes for Paper Setting:

1. Set the Questions as per the Syllabus/Scheme provided and confine it to the syllabus.
2. Question paper should cover entire syllabus as per the requirement of Outcome Based Education with 40% of total marks to bloom's cognitive levels 1 & 2, 40% for bloom's cognitive levels 3 & 4 and 20% to bloom's cognitive levels 5 & 6.
3. **Question ONE:** Question One has 20 sub questions of one marks each covering all modules. Four questions from each module have to be set. The questions have to be set such that the expected answer could be a *One word answer/One sentence answer/Definitions/Fill in the blank*. Multiple choice questions and Match the Following may be avoided.
4. There shall be a maximum of four sub questions in each question(Question 2 to 8) Kindly indicate the distribution of marks for each subdivision.
5. Kindly prepare the Question paper in MS-Word Format, burn it in the CD provided, take a print and affix the signature for authentication
6. There is no internal choice in Modules 1, 2 and 3 Whereas internal choice has to be given in module 4 (Question 5 OR 6) and module 5 (Question 7 OR 8)
7. If the questions include any sketches kindly draw it with black pen in the print out of the question paper, specify figure no. and include it in the question.



GLOBAL ACADEMY OF TECHNOLOGY

NAAC accredited with "A" Grade

Approved by AICTE, New Delhi, Recognized by Govt. of Karnataka & Affiliated to Visvesvaraya Technological University, Belagavi
Ideal Homes Township, Rajarajeshwari Nagar, Bengaluru- 560098.



For Oneday workshop on
"New Approach to the Revised Assessment & Accreditation of NAAC"



in association with




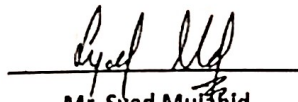
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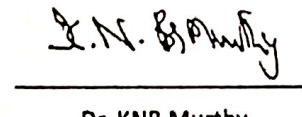
Mrs. Manjula B G

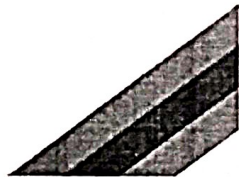
This is to certify that _____

of KSSEM, Bengaluru has successfully attended
/ participated in workshop on the "New Approach to the Revised Assessment & Accreditation of NAAC" held at
Global Academy of Technology, Ideal Homes Township, Mysore Road, Rajarajeshwari Nagar, Bengaluru - 560 098,
Karnataka on January 4th, 2019.


Dr. N. Ranapratap Reddy
Principal, GAT


Mr. Syed Mujahid
Director - ICARE RATINGS


Dr. KNB Murthy
Chairman, BITES



Design and Development of Data Acquisition and Controlling Unit for Power Generation

Manjula.B.G^{#1}, Srikant Shanu^{#2}, Shradha J^{#3}, Vishal B.V^{#4}, Ayesha Firdose^{#5}

^{#1}Associate Professor, Electrical & Electronics Engineering, K.S.S.E.M, Bengaluru, Karnataka state, India

^{#2, #3, #4, #5} Student, Electrical & Electronics Engineering, K.S.S.E.M, Bengaluru, Karnataka State, India.

Corresponding Author: Manjula.B.G

ABSTRACT —The most important factor to be considered for economic and efficient generation of power depends on the effective monitoring and controlling of various parameters such as voltage, current, power and power factor. Continuous monitoring of these parameters provides us information regarding factors which affect the generation and transmission of power. The optimal adjustments of these parameters can enable us to improve efficiency and to achieve remarkable energy savings.

The Designed Data Acquisition and Control unit for power generation was simulated in LabVIEW 2016 Pro Software which is interfaced with hardware through serial communication port. The circuit consisting Micro-Controller, LCD, Transformer, Relay, current transformer built on wooden board. All the connections are made and soldered so as to maintain proper stable circuit connection. The required supply is provided to all components used in circuit. Different sensors are used to control different parameters. The simulation of power plant is achieved by using LabVIEW software.

Keywords: LabVIEW, Microcontroller, LCD, voltage sensor, current sensor.

Date of Submission: 05-07-2019

Date of Acceptance: 21-07-2019

I. INTRODUCTION

A power plant is a facility used to generate electric power with the help of one or more generators which converts different energy sources into electrical power. Electricity is a secondary energy source which means that electricity is obtained from the conversion of other primary sources of energy such as coal, hydel natural gas, nuclear, solar or wind energy. The energy sources can be renewable or non-renewable.

A power plant or generating station is necessary for the generation of electric power. Most power stations contain one or more generators, a rotating machine which converts mechanical power into electrical power. Hence monitoring and controlling the parameters involved in the process of generation is an important task. Our project is based on designing and developing of a data acquisition and controlling unit for power generation by using LabVIEW. Data acquisition is the process of sampling signals that measure real world physical conditions and converting the resulting samples into digital numeric values that can be accepted by a computer. Data acquisition systems, abbreviated by the acronyms DAS or DAQ typically convert analog waveforms into digital values for processing.

ATMega32 microcontroller is used in the hardware for conversion of all analog data from sensors to digital signals. It is an 8-bit high

performance microcontroller of Atmel's Mega AVR family. ATMega32 is based on enhanced reduced instruction set computing architecture with powerful instructions. Most of the instruction execute in one machine cycle. ATMega32 can work on a maximum frequency of 16MHz. The other hardware components include voltage sensors LM7805, current transformer, transformers and relays.

LabVIEW software which is basically used in virtual instrumentation domain. This system provides an efficient monitoring and data acquisition arrangement. It reduces the complexity in cable installation and maintenance costs. It provides a platform for secure and reliable data acquisition in critically controlled environment in power plant.

II. LITERATURE REVIEW

The use of data acquisition systems proposes a new concept of a low cost power generation applied to de-centralized renewable energy plants with an USB interface. The use of such systems contributes to disseminate these plants recognizing in real time local energy resources, monitoring energy conversion efficiency and sending information concerning failures. These aspects are important, mainly for developing countries, where decentralized power plants based on renewable sources are in some cases the best option for supplying electricity to rural areas.

KAMMAVARI SANGHAM GROUP OF INSTITUTIONS

STAFF SELF APPRAISAL REPORT

2018-2019

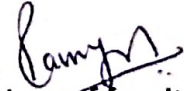
KSSEM


Field	Data	SCORE
Name	RAMYA K R	
Present Address, Mob.No., e-mail id.	#1517 Nandi Vinyas Apartment Block A 'GF3' Ankappa Layout Uttarahalli Main Road Bangalore 560061 9916901204 ramya.k.r@kssem.edu.in	---
Age and Date of Birth	37 years / 3-6-1982	
Qualification	M.Tech	
Designation and Department	Associate Professor, Electrical and Electronics Engineering	
Teaching Experience (After PG)	8 years	
Other Experience (If any)	1 year 6 months of teaching experience before PG	
List of Subjects Taught till date and percentage pass (use separate sheet if necessary)	List attached	
Subjects taught in the Assessment Year and percentage pass (10marks for each x Percentage)	1.Digital System Design – 85.71 2.Signals and Systems – 91.17 3.Operational Amplifier and Linear IC's- waiting for the result 4.Digital Signal Processing - waiting for the result	33/40
Details of UG Projects Guided (5 marks/ project guided)	1. Design and Development of Electromagnetic Robot with Four Wheel Drive System using PID Controller.	5 /10
Details of PG Projects Guided (5 marks/ project guided)	1.NA 2.NA	/10
Additional Inputs given in the class in addition to the syllabus (Give proof and justification) (If applicable)	-	/5
Guest / Invited Lectures arranged (2marks /lecture) Max	NIL	/5

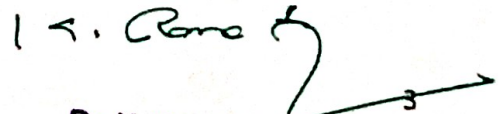
5 marks.		
Details of Industrial Visits arranged. (2marks/visit) Max 5 marks.	NIL	/5
Number of FDPs attended since joining service (Attach Separate List)	List attached	--
Details of students mentored during current assessment year.	Mentored ten Second year students	--
Details of Participation in VTU Bodies (2 Marks)	NIL	/2
Details on Examination related Activity (2marks each)	1. Practical Exams- Internal examiner and External Examiner 2. Conduction of Theory exams- Relieving Superident 3. Paper Setting - Nil 4. Evaluation-Involved in digital valuation work at KSIT	6/8
List of FDPs attended during the Assessment year (5 marks each) (Attach Certificate copies)	1) Six Days FDP on "Application of Advance Power Electronics for Power Systems, Power Quality and Electric Vehicles"	5/10
Financial Assistance received during current year for attending FDPs	Rs.0	--
Status of Ph.D. [Attach proof for each stage] (This can be claimed only once during a life time after the PhD is awarded) [Attach proof for every claim]	Appeared for Course work exams	3/10
Research Publications: (5 marks each) [Attach copies of Title Page]	1. Published a paper in International journal of Engineering Science Invention (IJESI)	5/10
Seminars / Workshops / Conferences attended (5 Marks each) [Attach Certificate Copies]	Attended one-week workshop on Research methodology.	5/10
Financial Assistance received during current year	Rs.0	--
Registered as Research Guide (Reasons for not registering)	NA	
No. of Research Scholars registered with details	NA	/5

Details of Patents Applied for (If any)	Nil	0/5
Academic Programs organized and supported during current year. (FDP/Workshop/Seminar / Conference)	Nil	0/5
Details of programs attended for skill development like MOOCs, MOODLES and others	Nil	0/5
Details of Utilization of NPTEL and other Online materials for augmenting own lectures.	Used NPTEL Videos to teach the topic filters.	5/5
Details of Project Proposal submitted during the current year. (At least one)	Nil	0/5
Details of Project Funds Received.	Rs.0	0/5
Consultancy Revenue Generated	Rs.0	0/5
Details of Participation in cultural events during the current year	1) Organized Dumb Charades event for students and staff during college fest Aarohana 2) Incharge for Green room on Aarohana Day	5/5
Additional Responsibilities in the Department/ College Example: Head, Coordinator etc.	1) Time Table coordinator 2) Class Teacher 3) Antisexual harrasement squad member 4) Morning Squad Duty 5) NAAC Criteria 1 department coordinator	10
Details of Live Membership for Professional Bodies (IEEE CSI SEA ISTE..)	LIFE MEMBER OF ISTE (LM94794)	5/5
Graduation Day Responsibilities. (If any) Please mention your role.		/5
TOTAL		87/190

Date: 17/7/2019


Signature of faculty


17/7/19
Associate Professor
Head of the Department
Department of Electrical & Electronics Engineering
KS School of Engineering and Management
Bengaluru-560 109


Dr. K. RAMA NARASIMHA
Principal/Director
KS School of Engineering and Management
Bengaluru - 560 109

STAFF SELF APPRAISAL REPORT**2018-2019****KSSEM**

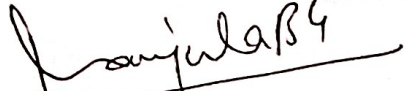
Field	Data	Score
Name	MANJULA B G	
Present Address, Mob. No., e-mail id.	#10, "Sri Vittala Chaithanya Nilaya , J.P.Nagar 7 th Phase, 7 th Main, Shreyas Colony, Bangalore, Karnataka State 560078 Mobile No: 8884123708 E-mail id: manjula.b.g@kssem.edu.in	
Age and Date of Birth	Forty-three and 15-07-1976	
Qualification	M.Tech	
Designation and Department	Associate Professor, Electrical & Electronics Engineering Department	
Teaching Experience (After PG)	7.5 years	---
Other Experience (If any)	1. Lecturer in Reddy Jana Sangha Polytechnic for 4 Years and 1 Months. 2. Trainee Engineer in Therelek Engineer's Pvt Ltd, Bangalore for 1 Year and 7 Months. 3. Guest Lecturer in Gurunanak Dev Government Polytechnic (Govt. of Delhi and NCR) New Delhi-5 months.	
List of Subjects Taught till date and percentage pass (use separate sheet if necessary)	Attached Annexure 1	
Subjects taught in the Assessment Year and percentage pass (10marks for each x Percentage)	1. Power System Operation & Control (15EE81) - 100 % 2. Power System Operation & Control (10EE82) - 100 % 3. Basic Electrical engineering (17ELE25) - Waiting for results 4. Electrical Machines Laboratory II (17EEL48) - Waiting for results 5. Utilization of Electrical Power (15EE742)-100 % 6. Basic Electrical engineering - 86% 7. Electrical Machines Laboratory I (17EEL37) - 100 % 8. Basic Electrical Engineering Laboratory (18ELE17) - 100 % 9. Electrical Power Utilization(10EE72)- 100 %	40 / 40


Details of UG Projects Guided (5 marks/ project guided)	Design and Development of transmission line protection unit using PLC.	5/10
Details of PG Projects Guided (5 marks/ project guided)	Not Applicable	/10
Additional Inputs given in the class in addition to the syllabus (Give proof and justification) (If applicable)	Model display	5 / 5
Guest / Invited Lectures arranged (2marks /lecture) Max 5 marks.	A Guest lecture was arranged for Electrical and Electronics Engineering students on 29/04/2018. Mr. Arun Kumar, Centre Manager from Edu CADD delivered a talk on "Application and Opportunities of CADD in Electrical Engineering"	2 / 5
Details of Industrial Visits arranged. (2marks/visit) Max 5 marks.	Indian Institute of Science (open day) visit on 23-3-2019	2 / 5
Number of FDPs attended since joining service (Attach Separate List)	Attached Annexure II	--
Details of students mentored during current assessment year.	10	--
Details of Participation in VTU Bodies (2 Marks)	-	/2
Details on Examination related Activity (2marks each)	1. Practical Exams: Appointed as internal and External lab examiner 2. Conduction of Theory exams: RS and DCS duty in 2018 theory exams 3. Paper Setting: Power System I(EE61) Dayanand Sagar College of Engineering May-June 2019 4. Evaluation: 10EE82 and18ELE13	8/8
List of FDPs attended during the Assessment year (5 marks each) (Attach Certificate copies)	Half a day Faculty Development Programme on "Learn 2 Learn" held on 19-07-2019 organized by department of Computer Science in association with Board for IT Education Standards at KSSEM.	5 /10
Financial Assistance received during current year for attending FDPs	NIL	--
Status of Ph.D. [Attach proof for each stage] (This can be claimed only once during a life time after the PhD is awarded)	1. Appeared for Course work exams (1 mark): Taken only two subjects 2. Applied for registration formalities (1 mark) 3. Identified Guide/Research Centre and preparing research Proposal (1mark.)	2 /10


[Attach proof for every claim]	4. Not thought of pursuing Ph.D. (zero)	
Research Publications: (5 marks each) [Attach copies of Title Page]	<ol style="list-style-type: none"> 1. "Design and Development of Data Acquisition and Controlling Unit for Power Generation" International Journal of Engineering Research, volume 9, issue 7, July 2019, pp 29-32, Doi 10.9790/9622-0907052932 ISSN: 2248 - 9622 2. "Design and Implementation of Rain Water, Humidity and Temperature Detector Alarm Unit for Green House Irrigation System" SSRG International Journal of Electrical and Electronics Engineering (SSRG-IJEEE), Page 19-22, Volume 4 Issue 5, May 2017 ISSN: 2348-8379. 3. "Design and Development of a Capacitive Power Transfer for Contactless Charging of Low Power Devices" SSRG International Journal of Electrical and Electronics Engineering, Volume 3 Issue 5, Page 41-44, May 2016, ISSN: 2348-8379. 4. "Contingency Ranking in Power Systems Employing Fuzzy Based Analysis" International journal of Electrical Engineering and Embedded systems, Vol.4, Jan-June 2012, Page No.1-7, ISSN:0975-4830 	10 / 10
Seminars / Workshops / Conferences attended (5 Marks each) [Attach Certificate Copies]	<ol style="list-style-type: none"> 1. One Week Workshop on "Research Methodology" held from 30th July to 4th August 2018 at VTU Human Resource Development Centre for Post Graduate Studies, Muddenahalli, Chikkaballapur District. 2. One day workshop on "New Approach to the Revised Assessment & Accreditation of NAAC" held at Global Academy of Technology on January 4th, 2019 	10/10
Financial Assistance received during current year	NIL	--
Registered as Research Guide (Reasons for not registering)	-	
No. of Research Scholars registered with details	-	/5
Details of Patents Applied for (If any)	-	/5
Academic Programs organized and supported during current year. (FDP/Workshop/Seminar / Conference)	Workshop on "Embedded System for IOT Applications" 25th & 26th March 2019 at KSSEM	5/5

Details of programs attended for skill development like MOOCs, MOODLES and others	-	/5
Details of Utilization of NPTEL and other Online materials for augmenting own lectures.	1. Power System Operation and Control: http://nptel.ac.in/courses/108104052/3 2. Basic Electrical Engineering: http://nptel.ac.in/courses/108108076/ 3. Electrical power utilization: www.nptelvideos.in/2012/11/illumination-engineering.html	5/5
Details of Project Proposal submitted during the current year. (At least one)	Preparation of project proposal for the projects under PMKVY (Pradhan Mantri Koushal Vikas Yojana) submitted to AICTE under power sector for academic year 2018-19.	5/5
Details of Project Funds Received.	-	/5
Consultancy Revenue Generated	-	/5
Details of Participation in cultural events during the current year	1. In charge of Aarohana 2019 Stage Committee. 2. Organizing Voice of KSSEM, Sketching and Dumb charades for Staff and Students. 3. Appointed as a Judge for Staff Rangoli competition in Aarohana 2019	5/5
Additional Responsibilities in the Department/ College Example: Head, Coordinator etc.	1. In-charge Head of Electrical and Electronics Engineering Department from 1-2-2019 to 30-5-2019. 2. NAAC 2 Coordinator, News Letter Coordinator	10
Details of Live Membership for Professional Bodies (IEEE CSI SEA ISTE	Life member of Indian Society for Technical Education. Membership No. LM94795	5/5
Graduation Day Responsibilities. (If any) Please mention your role.	1. Member of Stage organizing committee 2. Preparation of prize list required for the different events like student data, best outgoing student from department, best student from each semester and so on for the department.	5/5
TOTAL		129/190

Date: 17-7-2019


Signature of faculty


17/7/19
Associate Professor
Head of the Department
Department of Electrical & Electronics Engineering
KS School of Engineering and Management
Bengaluru-560 109


Dr. K. RAMA NARASIMHA
Principal/Director
KS School of Engineering and Management
Bengaluru - 560 109

STAFF SELF APPRAISAL REPORT

2018-2019

KSIT/KSSEM

Field	Data	SCORE
Name	ANIL KUMART . R .B	
Present Address, Mob.No., e-mail id.	#681/16 6 th cross ,Bhagath Singh Nagar,Near Dankay Park,Behind Chetana School , Nittuvalli Road Davangere-577002 8197357906 anilkumar.trb@kssem.edu.in	
Age and Date of Birth	35 & 17-05-1985	---
Qualification	BE.,MTech.,MISTE	
Designation and Department	Assistant Professor and Electrical and Electronics	
Teaching Experience (After PG)	5years,7 months	
Other Experience(If any)	6months in Industry	
List of Subjects Taught till date and percentage pass (use separate sheet if necessary)	1.EEMI 2.Transmission and distribution 3 Switch gear and protection 4.CAED 5.Power system planning 6.Electrical design and estimating and costing 7.Building services -2 8.Electric Power generation 9.Introduction to nuclear power 10.High voltage Engineering 11.Basic Electrical Engineering 12.Basic electrical lab 13.power electronics lab 14.high voltage and relay lab 15.Pspice lab 16.CAED LAB 17.Renewable Energy Resources 18.AEC LAB	
Subjects taught in the Assessment Year and percentage pass (10marks for each x Percentage)	1.Electrical Design Estimating and Costing-100% 2.Basic Electrical Engineering –Awaiting results 3. Building Services –II (BArchitecture) -100%	40/40

	4. High voltage Engineering-97.45% 5. Basic Electrical Laboratory-100% 6. Power Electronics Laboratory-100%	-
Details of UG Projects Guided (5 marks/ project guided)	1. Design and development of induction motor Fault detection system using IoT 2. Design and development of crop protection and proper usage of rain water using GSM	10/10
Details of PG Projects Guided (5 marks/ project guided)	1. NA 2.	/10
Additional Inputs given in the class in addition to the syllabus (Give proof and justification) (If applicable)	1. ELECTRICAL CONTROL PANEL	5/5
Guest / Invited Lectures arranged (2marks /lecture) Max 5 marks.	NIL	/5
Details of Industrial Visits arranged. (2marks/visit) Max 5 marks.	1. VARAHI POWER STATION UDUPI DISTRICT 2. LINGANMUKHI DAM	4/5
Number of FDPs attended since joining service (Attach Separate List)	6	--
Details of students mentored during current assessment year.	9	--
Details of Participation in VTU Bodies (2 Marks)	NIL	/2
Details on Examination related Activity (2marks each)	1. Practical Exams-INTERNAL AND EXTERNAL 2. Conduction of Theory exams-INVIGILATOR 3. Paper Setting- PAPER SETTER 4. Evaluation-EVALUATOR	8/8
List of FDPs attended during the Assessment year (5 marks each) (Attach Certificate copies)	1) NIL 2)	/10
Financial Assistance received during current year for attending FDPs	Rs .NIL	--
Status of Ph.D.	1. Awarded (2 marks)	

<p>[Attach proof for each stage] (This can be claimed only once during a life time after the PhD is awarded) [Attach proof for every claim]</p>	<ol style="list-style-type: none"> 2. Thesis Submitted and awaiting reports (1 mark) 3. Thesis Preparation (2 Mark) 4. Experimentation/Data Collection in completed (1 mark) 5. Comprehensive viva voce completed (1 mark) 6. Appeared for Course work exams (1 mark) 7. Applied for registration formalities (1 mark) 8. Identified Guide/Research Centre and preparing research Proposal (1mark.) 9. Not thought of pursuing Ph.D. (zero) 	<p style="text-align: center;">/10</p>
<p>Research Publications: (5 marks each) [Attach copies of Title Page]</p>	<p style="text-align: center;">NIL</p>	<p style="text-align: center;">/10</p>
<p>Seminars / Workshops / Conferences attended (5 Marks each) [Attach Certificate Copies]</p>	<p style="text-align: center;">NIL</p>	<p style="text-align: center;">/10</p>
<p>Financial Assistance received during current year</p>	<p style="text-align: center;">NO</p>	<p style="text-align: center;">--</p>
<p>Registered as Research Guide (Reasons for not registering)</p>	<p style="text-align: center;">NA</p>	
<p>No. of Research Scholars registered with details</p>	<p style="text-align: center;">NA</p>	<p style="text-align: center;">/5</p>
<p>Details of Patents Applied for (If any)</p>	<p style="text-align: center;">NIL</p>	<p style="text-align: center;">/5</p>
<p>Academic Programs organized and supported during current year. (FDP/Workshop/Seminar / Conference)</p>	<p style="text-align: center;">ELECTRICAL CAED SEMINAR</p>	<p style="text-align: center;">5/5</p>
<p>Details of programs attended for skill development like MOOCs, MOODLES and others</p>	<p style="text-align: center;">NIL</p>	<p style="text-align: center;">/5</p>
<p>Details of Utilization of NPTEL and other Online materials for augmenting own lectures.</p>	<p style="text-align: center;">NPTEL VIDEO FOR HIGH VOLTAGE ENGINEERING AND BASIC ELECTRICAL ENGINEERING</p> <p>http://www.nptelvideos.com/lecture.php?id=7776 http://www.nptelvideos.com/lecture.php?id=7778</p>	<p style="text-align: center;">5/5</p>

Details of Project Proposal submitted during the current year. (At least one)	NIL	/5
Details of Project Funds Received.	NO	/5
Consultancy Revenue Generated	NO	/5
Details of Participation in cultural events during the current year	1)DUMCHARDES-2 nd place 2)TREASURER HUNT	5/5
Additional Responsibilities in the Department/ College Example: Head, Coordinator etc.	1)WEBSITE COORDINATOR 2)NAAC-2 COORDINATOR 3)MENTOR	10
Details of Live Membership for Professional Bodies (IEEE CSI SEA ISTE	MISTE -LM89355	5/5
Graduation Day Responsibilities. (If any) Please mention your role.	1. Stage committee-Aarohana-19 2. Ropes huds and caps-for graduation day 3. Momentos-Responsibilities	5/5
TOTAL		92/103

Date:19-7-2019

Signature of faculty

Anil Kumar

[Signature]
19/7/2019

Associate Professor
Head of the Department
Department of Electrical & Electronics Engineering
K S School of Engineering and Management
Bengaluru-560 109

[Signature]
Dr. K. RAMA NARASIMHA
Principal/Director
K S School of Engineering and Management
Bengaluru - 560 109

STAFF SELF APPRAISAL REPORT**2018-2019****KSSEM**

Field	Data	SCORE
Name	GOUSIA SULTANA	---
Present Address, Mob.No., e-mail id.	41, Janapriya abodes, Kenchenahalli, R.R.Nagar-560098 Mob NO: 8095459787 e-mail id: gousia.sultana@kssem.edu.in	
Age and Date of Birth	Age-30, 02-12-1988	
Qualification	B.E, M.Tech, (pursuing Ph.D)	
Designation and Department	Assistant Professor Electrical & Electronics Engineering	
Teaching Experience (After PG)	5 years	
Other Experience (If any)	4 years (Teaching experience before PG)	
List of Subjects Taught till date and percentage pass (use separate sheet if necessary)	(ANNEXURE 1)	
Subjects taught in the Assessment Year and percentage pass (10marks for each x Percentage)	<ol style="list-style-type: none"> 1. Power system analysis-2 ----- 94.4% 2. Microcontrollers ----- 91.7% 3. Microcontrollers lab ----- 100% 4. Basic Electrical lab ----- 100% 5. Integration of Distributed Generation ----- --- 100% 6. Control Systems (waiting for results) 7. Control system lab (waiting for results) 	39/40
Details of UG Projects Guided (5 marks/ project guided)	<ol style="list-style-type: none"> 1. Study of Bangalore City Transmission Network with respect of Under Voltage-Remedial Measures. <ul style="list-style-type: none"> • The project has been awarded as Third best project in inter college project exhibition held in KSSEM on 20th May 2019. • This project was conducted in the KPTCL, we have developed a Bangalore city transmission network model using Mipower software. • After simulation by running the LFA, under voltage faults are detected and reported. • By using capacitor banks in the necessary 	10/10

	<p>locations the under voltage faults has been reduced and same report has been produced to KPTCL.</p> <ul style="list-style-type: none"> This report leads for the development and addition of transmission lines, reduction of under voltage faults and helps for the addition of RES. 	
Details of PG Projects Guided (5 marks/ project guided)	NA	/10
Additional Inputs given in the class in addition to the syllabus (Give proof and justification) (If applicable)	<ol style="list-style-type: none"> Used NPTEL video lectures. Experimental demonstration of one of the theory concepts in the laboratory for control system subject in addition to the syllabus. Videos related to the subject integration of distributed generation for final year students Conducted Quiz competitions to enhance student knowledge in respective subject. 	4/5
Guest / Invited Lectures arranged (2marks /lecture) Max 5 marks.	NIL	/5
Details of Industrial Visits arranged. (2marks/visit) Max 5 marks.	NIL	/5
Number of FDPs attended since joining service (Attach Separate List)	5 (ANNEXURE 2)	--
Details of students mentored during current assessment year.	Mentored 10 students of final year (in both 7 th and 8 th Semester)	--
Details of Participation in VTU Bodies (2 Marks)	Worked as External Deputy Chief Superintendent of VTU exam from June to July 2019	2/2
Details on Examination related Activity (2marks each)	<ol style="list-style-type: none"> Practical Exams – worked as internal and external examiner for lab exams. Project Exams – worked as internal project examiner for the conduction of project viva. Conduction of Theory exams – <ul style="list-style-type: none"> External DCS for conduction of theory exam. Room Invigilator for theory exams. Evaluation –paper evaluation of two subjects. 	8/8

List of FDPs attended during the Assessment year (5 marks each) (Attach Certificate copies)	1) Attended 5-day QIP short term course on "Solar Energy Conversion and Storage" during 11-15 th June 2018 at IISc, Bengaluru. (ANNEXURE 3)	5/10
		--
Status of Ph.D. [Attach proof for each stage] (This can be claimed only once during a life time after the PhD is awarded) [Attach proof for every claim]	1. Awarded (2 marks) 2. Thesis Submitted and awaiting reports (1 mark) 3. Thesis Preparation (2 Mark) 4. Experimentation/Data Collection in completed (1 mark) 5. Comprehensive viva voce completed (1 mark) 6. Appeared for Course work exams (1 mark) 7. Applied for registration formalities (1 mark) 8. Identified Guide/Research Centre and preparing research Proposal (1mark.) 9. Not thought of pursuing Ph.D. (zero) (ANNEXURE 4)	4/10
Research Publications: (5 marks each) [Attach copies of Title Page]	NO. of research publications: 7 (ANNEXURE 5)	5/10
Seminars / Workshops / Conferences attended (5 Marks each) [Attach Certificate Copies]	NO. of Seminars / Workshops / Conferences attended : 10 (ANNEXURE 6)	10/10
Financial Assistance received during current year	NIL	--
Registered as Research Guide (Reasons for not registering)	NA	
No. of Research Scholars registered with details	NA	/5
Details of Patents Applied for (If any)	NIL	/5
Academic Programs organized and supported during current year.	Supported, guided and coordinated 6 students for participation in the Inter-collegiate climate and environment quiz competition held on 11 th	5/5

(FDP/Workshop/Seminar/Conference)	September 2018 organized and held by IISC. (ANNEXURE 7)	
Details of programs attended for skill development like MOOCs, MOODLES and others	Registered for NPTEL online course for subjects: 1. DC Microgrid 2. Control System	2/5
Details of Utilization of NPTEL and other Online materials for augmenting own lectures.	NPTEL videos augmented for the subjects 1. Microcontrollers 2. Power system analysis 3. Control systems.	5/5
Details of Project Proposal submitted during the current year. (At least one)	NIL	/5
Details of Project Funds Received.	NIL	/5
Consultancy Revenue Generated	NIL	/5
Details of Participation in cultural events during the current year	1) Organized quiz event for Aarohana 2019. 2) Worked as coordinator of stage committee for Aarohana 2019. 3) In-charge of Green room on Aarohana-2019	5/5
Additional Responsibilities in the Department/ College Example: Head, Coordinator etc.	1) Class teacher for final year students. 2) NAAC coordinator. 3) Mentor for both 7 th and 8 th semesters. 4) Control system Lab In-charge. 5) Microcontroller lab In-charge. 6) Squad duty 7) Coordinator for the Alumini meet 2019	10/ 10
Details of Live Membership for Professional Bodies (IEEE CSI SEA ISTE	• Member of GIAN (Global initiative of academic network)	2/5
Graduation Day Responsibilities. (If any) Please mention your role.		/5
TOTAL		117/190

Date: 18/07/2019.

[Signature]
18/07/19

Associate Professor
Head of the Department
Department of Electrical & Electronics Engineering
KS School of Engineering and Management
Bengaluru-560 109

[Signature]
Signature 18/07/2019

[Signature]
4
Dr. K. RAMA NARASIMHA
Principal/Director

KS School of Engineering and Management
Bengaluru - 560 109

STAFF SELF APPRAISAL REPORT**2018-2019****KSIT/KSSEM**

Field	Data	SCORE
Name	Shruti V Joshi	
Present Address, Mob.No., e-mail id.	#114 shreenilayam III, near bande mutt, kommaghatta road, kengeri satellite toem, Bengaluru- 560060 Phone- 8861245655 Email- joshishrut@gmail.com	---
Age and Date of Birth	33 years, 5/6/1986	
Qualification	Mtech	
Designation and Department	Assistant Professor , EEE	
Teaching Experience (After PG)	3 years	
Other Experience(If any)	Taught in ITI and GTTC for 6 months	
List of Subjects Taught till date and percentage pass (use separate sheet if necessary)	Electrical power Generation – 98.6 % Basic Eelctrical Engineering – 97.66 % Transmission and Distribution Power System Analysis -1 Transformer and Induction Motors Lab DC machines and Synchronous machines lab Analog Electronics lab Control systems and Measurements Lab. Power Ssystem and Simulation Lab Power Eelectronics Lab	
Subjects taught in the Assessment Year and percentage pass (10marks for each x Percentage)	1. Transformers and Generators - 92.85 % 2. Basic Electrical Engineering - 79.5 % 3. Basic Electrical Lab – 100% 4. Electrical machines lab 1 – 100% 5. Power System Analysis I 6. Renewable Energy Sources 7. Control sstems lab	37.23/40
Details of UG Projects Guided (5 marks/ project guided)	1. 2.	/10
Details of PG Projects	1.	/10


Guided (5 marks/ project guided)	2.	
Additional Inputs given in the class in addition to the syllabus (Give proof and justification) (If applicable)	Nptel vedios were shown Few questions link from electrical4u website were given. Paper models were shown foe basic electrical engineering	3/5
Guest / Invited Lectures arranged (2marks /lecture) Max 5 marks.		/5
Details of Industrial Visits arranged. (2marks/visit) Max 5 marks.		/5
Number of FDPs attended since joining service (Attach Separate List)	4	--
Details of students mentored during current assessment year.	10 students of 3 rd year mentored	--
Details of Participation in VTU Bodies (2 Marks)		/2
Details on Examination related Activity (2marks each)	1. Practical Exams Went as external examiner to DSATM 2.Paper valuation of T&D in 2015.	2/8
List of FDPs attended during the Assessment year (5 marks each) (Attach Certificate copies)	1)3 days FDP on Aspects of Effective Programming and Research.at KSSEM 2) FDP on Application of advanced power electronics for power systems, power quality and Electric Vehicles. At SJBIT bangalore	10/10
Financial Assistance received during current year for attending FDPs'	Rs.	--
Status of Ph.D. [Attach proof for each stage] (This can be claimed only once during a life time after the PhD is awarded) [Attach proof for every claim]	1. Awarded (2 marks) 2. Thesis Submitted and awaiting reports (1 mark) 3. Thesis Preparation (2 Mark) 4. Experimentation/Data Collection in completed (1 mark) 5. Comprehensive viva voce completed (1 mark) 6. Appeared for Course work exams	/10

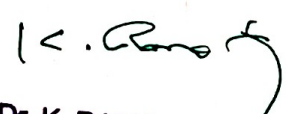
	(1 mark) 7. Applied for registration formalities (1 mark) 8. Identified Guide/Research Centre and preparing research Proposal (1mark.) 9. Not thought of pursuing Ph.D. (zero)	-
Research Publications: (5 marks each) [Attach copies of Title Page]	1. 2.	/10
Seminars / Workshops / Conferences attended (5 Marks each) [Attach Certificate Copies]		/10
Financial Assistance received during current year	Rs.	--
Registered as Research Guide (Reasons for not registering)	Yes / No	-
No. of Research Scholars registered with details		/5
Details of Patents Applied for (If any)		/5
Academic Programs organized and supported during current year. (FDP/Workshop/Seminar / Conference)	Department coordinator for programming training given to 4 th sem students.	1/5
Details of programs attended for skill development like MOOCs, MOODLES and others		/5
Details of Utilization of NPTEL and other Online materials for augmenting own lectures.	NPTEL vedios on Transformers and Generators. 1. https://youtu.be/LeME2q2E4U8 2. https://www.youtube.com/watch?v=0kaKoNLoHLk 3. https://www.youtube.com/watch?v=R3HUkHq_WIA	5/5
Details of Project Proposal		/5

submitted during the current year. (At least one)		
Details of Project Funds Received.	Rs.	- /5
Consultancy Revenue Generated	Rs.	/5
Details of Participation in cultural events during the current year	1) participated in cooking without fire 2) participated in Rangoli competition 3) participated in dumcharads 4) participated in treasure hunt 5) organized voice of KSSEM competiton	5/5
Additional Responsibilities in the Department/ College Example: Head, Coordinator etc.	1) Project coordinator 2) research coordinator 3) NAAC criteria 3 4) Co Ordinator for programing through solutions for 4 th sem 5) department representative for admission Desk. 6) Placement Co Ordinator.	10
Details of Live Membership for Professional Bodies (IEEE CSI SEA ISTE)		/5
Graduation Day Responsibilities. (If any) Please mention your role.	Help Desk duty for EEE department.	2/5
TOTAL		75.25/190

Date: 22/7/19


Signature of faculty


Associate Professor
Head of the Department
Department of Electrical & Electronics Engineering
KS School of Engineering and Management
Bengaluru-560 109


Dr. K. RAMA NARASIMHA
Principal/Director
KS School of Engineering and Management
Bengaluru - 560 109

STAFF SELF APPRAISAL REPORT

2018-2019

KSIT/KSSEM

Field	Data	SCORE
Name	TEJASWINI G V	
Present Address, Mob.No., e-mail id.	#15, Gangamma Nilaya, 7 th main, 2 nd cross, near KEB, Soudhamini Layout, Konanakunte, Bangalore -560 062 Mob. No.: 7349472246 e-mail ID: tejaswini.gv@kssem.edu.in	
Age and Date of Birth	31 years, 20/04/1988	---
Qualification	BE., M.Tech.,	
Designation and Department	Assistant Professor, Electrical and Electronics Engineering	
Teaching Experience (After PG)	2.6 years	
Other Experience(If any)	-	
List of Subjects Taught till date and percentage pass (use separate sheet if necessary)	<ol style="list-style-type: none"> 1. Utilization of Electric Power(twice) 2. Microcontroller 8051 3. Renewable energy sources 4. Basic Electrical Engineering 5. Industrial Drives and Applications 6. Power System Planning 7. Electric Motors 8. Energy Auditing and Demand Side Management 9. DC machines and Synchronous laboratory 10. Microcontroller laboratory 11. Power system simulation laboratory 12. Basic Electrical Engineering Laboratory 	
Subjects taught in the Assessment Year and percentage pass (10marks for each x Percentage)	<ol style="list-style-type: none"> 1. Industrial drives and Applications(10EE74)-100% 2.Power System Planning(10EE761)-100% 3. Basic Electrical Engineering laboratory (18EEL17) - (100%) 4. Electric Motors(17EE44)- Awaiting 5.Energy auditing and Demand Side Management(10EE842)-100% 6. Electric Machines II laboratory (17EEL47) - Awaiting 7. Basic Electrical Engineering lab (18EEL27)- Awaiting 	40/40


Details of UG Projects Guided (5 marks/ project guided)	1. Design and Development of Electromagnetic Robot with four wheels Drive applied PID System (Secured second place in Department project exhibition held at KSSEM on 20 th May 2019) 2.	5/10
Details of PG Projects Guided (5 marks/ project guided)	1. 2.	/10
Additional Inputs given in the class in addition to the syllabus (Give proof and justification) (If applicable)	-	/5
Guest / Invited Lectures arranged (2marks /lecture) Max 5 marks.	-	/5
Details of Industrial Visits arranged. (2marks/visit) Max 5 marks.	-	/5
Number of FDPs attended since joining service (Attach Separate List)	-	--
Details of students mentored during current assessment year.	Mentored 13 students of 1 st year EEE Department.	--
Details of Participation in VTU Bodies (2 Marks)	-	/2
Details on Examination related Activity (2marks each)	1. Practical Exams 2. Conduction of Theory exams (As an Invigilator) 3. Paper Setting 4. Evaluation	2/8
List of FDPs attended during the Assessment year (5 marks each) (Attach Certificate copies)	1) Five Days on "BOSCH REXROTH PLC, SERVODRIVES, SENSORS AND SCADA" at Dayananda Sagar Academy of Technology and Management from (16 th to 20 th) January, Annexure-1 2)	5/10
Financial Assistance received during current year for attending FDPs	Rs. Nil	--
Status of Ph.D. [Attach proof for each]	1. Awarded (2 marks) 2. Thesis Submitted and awaiting reports (1 mark)	


stage] (This can be claimed only once during a life time after the PhD is awarded) [Attach proof for every claim]	<ol style="list-style-type: none"> 3. Thesis Preparation (2 Mark) 4. Experimentation/Data Collection in completed (1 mark) 5. Comprehensive viva voce completed (1 mark) 6. Appeared for Course work exams (1 mark) 7. Applied for registration formalities (1 mark) 8. Identified Guide/Research Centre and preparing research Proposal (1mark.) 9. Not thought of pursuing Ph.D. (zero) 	/10
Research Publications: (5 marks each) [Attach copies of Title Page]	<ol style="list-style-type: none"> 1. "Simulation of 13 panels phased array antenna by using STK Tool" Published in International academy of science engineering and Technology, ISSN 2278-9901, Vol. 2, Issue 4, Annexure-2 2. "Development of VAR/p.f. Regulator for Brushless AC Generator to be paralleled with Grid" published in International Journal of Advance Engineering and Research Development, e-ISSN : 2348-4470, p- ISSN: 2348-6406, Annexure-3 	10/10
Seminars / Workshops / Conferences attended (5 Marks each) [Attach Certificate Copies]	-	/10
Financial Assistance received during current year	Rs. Nil	--
Registered as Research Guide (Reasons for not registering)	Yes / No	No
No. of Research Scholars registered with details	-	/5
Details of Patents Applied for (If any)	-	/5
Academic Programs organized and supported during current year. (FDP/Workshop/Seminar/Conference)	-	/5

Details of programs attended for skill development like MOOCs, MOODLES and others	Introduction to Smart Grid course registered Electrical Machines course registered	3/5
Details of Utilization of NPTEL and other Online materials for augmenting own lectures.	<u>Industrial Drives and Applications</u> http://www.nptelvideos.in/2012/11/industrial-drives-power-electronics.html <u>Electric Motors</u> http://www.nptelvideos.com/lecture.php?id=7776 http://www.nptelvideos.com/lecture.php?id=7778 http://www.nptelvideos.com/lecture.php?id=7781	5/5
Details of Project Proposal submitted during the current year. (At least one)	--	/5
Details of Project Funds Received.	Rs. Nil	/5
Consultancy Revenue Generated	Rs. Nil	/5
Details of Participation in cultural events during the current year	1) Rangoli Competition 2) Cooking without fire 3) Dumbcharades (Secured 2 nd place), Annexure-4 4) Treasure hunt 5) Pencil Sketching Co-ordinator	5/5
Additional Responsibilities in the Department/ College Example: Head, Coordinator etc.	1) Internship Co-ordinator for the department 2) NAAC Criteria-5 Co-ordinator 3) Forum Co-ordinator for the department	10
Details of Live Membership for Professional Bodies (IEEE CSI SEA ISTE)	Life Member for ISTE, Membership No.-LM 126378 Annexure-5	5/5
Graduation Day Responsibilities. (If any) Please mention your role.	Help Desk, Monitoring Squad	5/5
TOTAL		95/190

Date: 20/7/2019

Signature of faculty


20/7/2019
Associate Professor
Head of the Department
Department of Electrical & Electronics Engineering
K S School of Engineering and Management
Bengaluru-560 109


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Dr. K. RAMA NARASIMHA
Principal/Director
K S School of Engineering and Management
Bengaluru - 560 109

STAFF SELF APPRAISAL REPORT

2018-2019

KSIT/KSSEM

Field	Data	SCORE
Name	PRATHIKSHA	
Present Address, Mob.No., e-mail id.	Flat No.301, Dhanush Grand Apartment, Nobel Residency Road Doddakammanahalli Main Off Bannerghatta Road Bangalore-560076 MOB NO. 8105951722 EMAIL ID:prathiksha@kssem.edu.in	---
Age and Date of Birth	29 years 23/04/1991	
Qualification	B.E , M.Tech	
Designation and Department	Assistant Professor Electrical and Electronics Engineering Dept.	
Teaching Experience (After PG)	2.5 years	
Other Experience(If any)	-	
List of Subjects Taught till date and percentage pass (use separate sheet if necessary)	<ol style="list-style-type: none"> 1.Basic Electrical Engineering 2.Linear IC's and Applications (twice) 3.Operational Amplifiers and Linear IC's 4.Transformer and Induction machines 5. Switchgear and Protection 6. Transformers and Generators 7. Power System Analysis and Stability 8. Power Generation and Economics (Result not announced) 9. Industrial drives and applications (94.44%) <p><u>Labs Handled:</u></p> <ol style="list-style-type: none"> 1.Transformers and Induction Machines 	

	<p>Lab (100%)</p> <p>2.DC Machines and Synchronous Machines</p> <p>Lab (100%)</p> <p>3.OPAMP and LIC Lab.(100%)</p> <p>4. Basic Electrical Engg Lab (100%)</p>	
<p>Subjects taught in the Assessment Year and percentage pass (10marks for each x Percentage)</p>	<p>1.Power Generation and Economics(17EE42) Result yet to be announced</p> <p>2.Industrial Drives and Applications(15EE82) – 94.4%</p> <p>3.Basic Electrical Engineering Lab(18EEL27)-100%</p> <p>4.Opamp and Linear IC's Lab(17EEL48)-100%</p>	29.44/40
<p>Details of UG Projects Guided (5 marks/ project guided)</p>	<p>1.Design and development of the Pneumatic powered wall Hang Painting Robot using Arduino and RF controller (2018-19)</p>	5/10
<p>Details of PG Projects Guided (5 marks/ project guided)</p>	<p>1. Not applicable</p> <p>2.</p>	/10
<p>Additional Inputs given in the class in addition to the syllabus (Give proof and justification) (If applicable)</p>	<p>Nptel Videos are used for PGE 17EE42 (turbines and steam power plant)and IDA 15EE82 (stepper motor drive and single phase fully controlled converter)Subjects</p>	5/5
<p>Guest / Invited Lectures arranged (2marks /lecture) Max 5 marks.</p>	NIL	/5
<p>Details of Industrial Visits arranged. (2marks/visit) Max 5 marks.</p>	NIL	/5
<p>Number of FDPs attended since joining service (Attach Separate List)</p>	<p>1. Participated in the FDP on "Outcome based Education and Bloom's Taxonomy" held during 4th and 5th December 2015 at KSSEM Bangalore.</p> <p>2. Participated in the FDP on "Training the Trainer Workshop on Intellectual Property Rights Significance for Academia held at KSSEM Bangalore on</p>	--

	<p>31st July 2015.</p> <p>3. Participated in workshop on "Review of Latest Trends in Transformer Design, Manufacture and CAD" held at KSSEM on 28th and 29th March 2016</p> <p>4. Participated in the FDP on "Application of Advance Power Electronics for Power Systems, Power Quality and Electric Vehicles" held At SJBIT Bangalore from 15th July to 20th July 2019.</p>	
Details of students mentored during current assessment year.	<p>Mentor for 7th semester students USN: 1kg15ee014 1kg15ee001,2,3,4,5,6,7,8,9 Total : 10 students</p>	--
Details of Participation in VTU Bodies (2 Marks)	NIL	/2
Details on Examination related Activity (2marks each)	<p>1. Practical Exams: External Examiner for BEE Lab Internal Examiner for Op-amp and LIC Lab</p> <p>2. Conduction of Theory exams-Invigilation Duty</p> <p>3. Paper Setting</p> <p>4. Evaluation</p>	4/8
List of FDPs attended during the Assessment year (5 marks each) (Attach Certificate copies)	<p>1) Participated in the FDP on "Application of Advance Power Electronics for Power Systems, Power Quality and Electric Vehicles" held at SJBIT Bangalore from 15th July to 20th July 2019.</p> <p>2)</p>	5/10
Financial Assistance received during current year for attending FDPs	Rs. NIL	--
Status of Ph.D. [Attach proof for each stage] (This can be claimed only once during a life time after the PhD is awarded)	<p>1. Awarded (2 marks)</p> <p>2. Thesis Submitted and awaiting reports (1 mark)</p> <p>3. Thesis Preparation (2 Mark)</p> <p>4. Experimentation/Data Collection in</p>	

[Attach proof for every claim]	<p>completed (1 mark)</p> <p>5. Comprehensive viva voce completed (1 mark)</p> <p>6. Appeared for Course work exams (1 mark)</p> <p>7. Applied for registration formalities (1 mark)</p> <p>8. Identified Guide/Research Centre and preparing research Proposal (1mark.)</p> <p>9. Not thought of pursuing Ph.D. (zero)</p>	/10
Research Publications: (5 marks each) [Attach copies of Title Page]	<p>1. NIL</p> <p>2.</p>	/10
Seminars / Workshops / Conferences attended (5 Marks each) [Attach Certificate Copies]	<p>Project work titled "Implementation of a Bi-directional Power Converter with Charging Feature and Simulation using MATLAB/Simulink" was selected for conference and resulted in the form of paper presentation at NMAMIT, Nitte during the International Conference on Emerging Trends in Engineering(ICETE-14) during 15th through 17th May, 2014</p> <p>2018-2019 NIL</p>	/10
Financial Assistance received during current year	NIL	--
Registered as Research Guide (Reasons for not registering)	Yes / No	
No. of Research Scholars registered with details	NIL	/5
Details of Patents Applied for (If any)	NIL	/5
Academic Programs organized and supported during current year. (FDP/Workshop/Seminar / Conference)	NIL	/5
Details of programs attended for skill development like MOOCs, MOODLES and others	NIL	/5
Details of Utilization of NPTEL and other Online materials for	Nptel Videos are used for PGE 17EE42 (turbines and steam power plant)and IDA	5/5

augmenting own lectures.	15EE82 (stepper motor drive and single phase fully controlled converter)Subjects	
Details of Project Proposal submitted during the current year. (At least one)	NIL	/5
Details of Project Funds Received.	Rs. NIL	/5
Consultancy Revenue Generated	Rs. NIL	/5
Details of Participation in cultural events during the current year	1) Participated in Dum charads and got 2 nd prize 2) Participated in Treasure Hunt 3) Co-ordinator for Voice of KSSEM	5/5
Additional Responsibilities in the Department/ College Example: Head, Coordinator etc.	1) Alumni Co-ordinator 2) Library co-ordinator 3) Lab Incharge for Op-amp and LIC Lab, Basic Electrical Engg Lab 4) NAAC Criteria 1 Co-ordinator 5) Cultural committee	10
Details of Live Membership for Professional Bodies (IEEE CSI SEA ISTE	NIL	/5
Graduation Day Responsibilities. (If any) Please mention your role.	NIL	/5
TOTAL		68.44/190

Date: 22/7/19

Prathibha
Signature of faculty

[Signature]
22/7/19
Associate Professor
Head of the Department
Department of Electrical & Electronics Engineering
KS School of Engineering and Management
Bengaluru-560 109

[Signature]
Dr. K. RAMA NARASIMHA
Principal/Director 5
KS School of Engineering and Management
Bengaluru - 560 109

STAFF SELF APPRAISAL REPORT**2018-2019****KSIT/KSSEM**

Field	Data	SCORE
Name	AKSHAY KUMAR D	
Present Address, Mob. No., e-mail id.	S/O DEVARAJU D C ,Dasarahalli (village),Masakanahalli(post),Hirisave (hobbli) Channarapatana(Taluk) Hassan (D) Pin code 573141	
Age and Date of Birth	26 Years 11 Months, 21/08/1992	
Qualification	B.E,M.TECH	
Designation and Department	Assistant professor, Electrical and Electronics Engineering	
Teaching Experience (After PG)	2 years	
Other Experience(If any)	Nil	
List of Subjects Taught till date and percentage pass (use separate sheet if necessary)	1) Basic electrical engineering lab(twice) 2) Analog electronics 3) Electrical estimation and costing 4) Transmission and distribution 5) Electrical machine design 6) Digital signal processing lab 7) Microcontroller lab	
Subjects taught in the Assessment Year and percentage pass (10marks for each x Percentage)	1) Analog electronics (71.82%) 2) Electrical estimation and costing (100%) 3) Basic Electrical Engineering Lab (100%) 4) Micro Controller Lab (100%) Annexure -1	37/40
Details of UG Projects Guided	1) Magnetic levitation model using	5/10


(5 marks/ project guided)	PID controller	
Details of PG Projects Guided (5 marks/ project guided)	Not applicable	/10
Additional Inputs given in the class in addition to the syllabus (Give proof and justification) (If applicable)	Components showed in the class like different types of switches, lamp holders, etc. PPTs	5/5
Guest / Invited Lectures arranged (2marks /lecture) Max 5 marks.	Nil	0/5
Details of Industrial Visits arranged. (2marks/visit) Max 5 marks.	Nil	0/5
Number of FDPs attended since joining service (Attach Separate List)	1	--
Details of students mentored during current assessment year.	9 students	--
Details of Participation in VTU Bodies (2 Marks)	Nil	0/2
Details on Examination related Activity (2marks each)	Room invigilator ,Internal test squad, Test coordinator	6/8
List of FDPs attended during the Assessment year (5 marks each) (Attach Certificate copies)	Nil	0/10
Financial Assistance received during current year for attending FDPs	Not received	--
	1. Awarded (2 marks)	

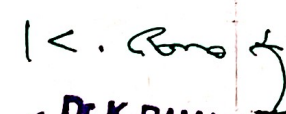
<p>Status of Ph.D.</p> <p>[Attach proof for each stage]</p> <p>(This can be claimed only once during a life time after the PhD is awarded)</p> <p>[Attach proof for every claim]</p>	<ol style="list-style-type: none"> 2. Thesis Submitted and awaiting reports (1 mark) 3. Thesis Preparation (2 Mark) 4. Experimentation/Data Collection in completed (1 mark) 5. Comprehensive viva voce completed (1 mark) 6. Appeared for Course work exams (1 mark) 7. Applied for registration formalities (1 mark) 8. Identified Guide/Research Centre and preparing research Proposal (1 mark.) 9. Not thought of pursuing Ph.D. (zero) 	<p>1/10</p>
<p>Research Publications: (5 marks each)</p> <p>[Attach copies of Title Page]</p>	<p>Nil</p>	<p>0/10</p>
<p>Seminars / Workshops / Conferences attended (5 Marks each) [Attach Certificate Copies]</p>	<p>FIVE day workshop on "Computational Methods for Partial Differential Equations using Mat Lab" organized by BNMIT in association with BITES</p> <p>Annexure-2</p>	<p>5/10</p>
<p>Financial Assistance received during current year</p>	<p>Not received</p>	<p>--</p>
<p>Registered as Research Guide (Reasons for not registering)</p>	<p>Not applicable</p>	
<p>No. of Research Scholars registered with details</p>	<p>Not applicable</p>	<p>/5</p>
<p>Details of Patents Applied for (If any)</p>	<p>Nil</p>	<p>/5</p>
<p>Academic Programs organized and supported during current year.</p>	<p>Two days Workshop on "Embedded system for IoT applications"</p> <p>Annexure -3</p>	<p>5/5</p>

(FDP/Workshop/Seminar / Conference)		
Details of programs attended for skill development like MOOCs, MOODLES and others	Nil	/5
Details of Utilization of NPTEL and other Online materials for augmenting own lectures.	NPTEL Videos , NESCO Academy videos	5/5
Details of Project Proposal submitted during the current year. (At least one)	Nil	0/5
Details of Project Funds Received.	Not received	0/5
Consultancy Revenue Generated	NO	/5
Details of Participation in cultural events during the current year	1) Dumb charades 2) Treasure hunt 3) Pencil Sketching Co-ordinator	5/5
Additional Responsibilities in the Department/ College Example: Head, Coordinator etc.	1) Class teacher 2) NAAC coordinator 3) Test coordinator	10
Details of Live Membership for Professional Bodies (IEEE CSI SEA ISTE	Nil	0/5
Graduation Day Responsibilities. (If any) Please mention your role.	Stage Committee.	5/5
TOTAL		89/190

Date: 19/7/2019

Ashay Kumar D
Signature of faculty
19/7/2019


19/7/2019
Associate Professor
Head of the Department
Department of Electrical & Electronics Engineering
K S School of Engineering and Management
Bengaluru-560 109


Dr. K. RAMA NARASIMHA
Principal/Director
K S School of Engineering and Management
Bengaluru - 560 109