

## Academic Calendar of VTU, Belagavi for ODD Semester of 2019-2020 (Jul 2019 – Jan 2020)

	I Sem B. E. / B. Tech. / B. Arch. (Tentative)	III, V & VII Sem B. E. /B. Tech. III, V, VII & IX Sem B. Arch.	III & V Sem MCA	III Sem MBA	III Sem M. Tech.	III Sem M. Arch.
Commencement of ODD Semester	01.08.2019	29.07.2019	29.07.2019	08.08.2019	26.08.2019	08.09.2019
Last Working day of ODD Semester	29.11.2019	30.11.2019	30.11.2019	05.12.2019	23.12.2019	06.01.2020
Practical Examinations	03.12.2019 To 13.12.2019	03.12.2019 To 13.12.2019	03.12.2019 To 07.12.2019	-	-	-
Theory Examinations	16.12.2019 To 04.01.2020	16.12.2019 To 07.02.2020	09.12.2019 To 28.12.2019	09.12.2019 To 04.01.2020	27.12.2019 To 10.01.2020	08.01.2020 To 22.01.2020
Internship Viva-Voce	-	-	-	-	12.01.2020 To 19.01.2020	-
Professional training / Organization study	-	-	-	-	-	-
Commencement of EVEN Semester	27.01.2020	10.02.2020	27.01.2020	27.01.2020	27.01.2020	01.02.2020

### NOTE

- VII Semester B. E / B. Tech students shall have to undergo **Internship** for a period of four Weeks.
  - I Semester B. E/ B. Tech / B. Arch Students shall compulsorily undergo **Induction Program** for a period of 3 Weeks (two phases) as per the schedule given by VTU. First phase 11 days in first semester and second phase 10 days in second semester.
1. College Time Table shall be arranged for five and a half week days and planned to accommodate EDUSAT transmission slots, the schedule of which will be notified separately.
  2. The faculty/staff shall be available to undertake any work assigned by the university.
  3. If any of the above date is declared to be a holiday then the corresponding event will come into effect on the next working day.
  4. Notification regarding Calendar of Events relating to the conduct of University Examination will be issued by the Registrar (Evaluation) from time to time.

*Buniga*  
20/1/19  
REGISTRAR



CALENDAR OF EVENTS: ODD SEMESTER (2019-2020)

SESSION: JUL 2019 – NOV 2019

Week No.	Month	Day						Days	Institutional Activities / Holidays
		Mon	Tue	Wed	Thu	Fri	Sat		
1	July/Aug	29	30	31	1	2	3DH	5	3 - Declared Holiday
2	Aug	5	6	7	8	9DH	10	5	9 - Varamaha Lakshmi (Declared Holiday) 10 - Monday Time Table
3	Aug	12H	13	14	15H	16	17	4	12 - Bakrid (Holiday) 15 - Independence Day 17 - Tuesday TimeTable
4	Aug	19	20	21	22	23	24DH	5	24 - Declared Holiday
5	Aug	26	27	28	29	30	31	6	31 - Wednesday Time Table
6	Sep	2H	3 T1	4 T1	5 T1	6 T1	7DH	4	2 - Vinayaka Chathrathi (Holiday) 7 - Declared Holiday
7	Sep	9 TA	10H	11	12	13	14	5	10 - Moharam (Holiday) 14 - Friday Time Table
8	Sep	16	17	18	19	20BV	21DH	5	16-20 First feedback 21 - Declared Holiday
9	Sep	23ASD	24 MA	25	26	27	28H	5	28 - Mahalaya Amavasya (Holiday)
10	Sep/Oct	30	1	2H	3	4	5*	5	2 - Gandhi Jayanthi (Holiday) 5-Ayudha Pooja in College 5 - Monday Time Table
11	Oct	7H	8H	9	10	11	12 TA	4	7 - Ayudha Pooja (Holiday) 8 - Vijaya Dashami (Holiday) 12 - Monday Time Table
12	Oct	14 T2	15 T2	16 T2	17T2	18	19DH	5	19 - Declared Holiday
13	Oct	21	22	23 BV	24 ASD	25 MA	26	6	26 - Tuesday Time Table
14	Oct/Nov	28	29H	30	31	1H	2DH	3	29 - Balipadyami (Holiday) 1 - Karmada Rajyothava (Holiday) 2 - Declared Holiday
15	Nov	4	5	6	7	8	9	6	9 - Tuesday Time Table 4-8 Second feedback
16	Nov	11	12	13	14	15H	16DH	4	15 - Kanakadasa Jayanthi (Holiday) 16 - Declared Holiday
17	Nov	18	19	20	21 LT	22 LT	23 LT	6	
18	Nov	25 T3	26 T3	27T3	28 T3	29 BV ASD	30* MA	6	30 - Thursday Time Table and Last Working Day
Total No. of Working Days: 89									

H	Holiday
BV	Blue Book Verification
T1, T2, T3	Tests 1,2,3
ASD	Attendance & Sessional Display
DH	Declared Holiday
LT	Lab Test
TA	Test Attendance
MA	Master Attendance Filling

Total Number of Working Days ( Excluding Holidays and Tests)

Monday	16
Tuesday	15
Wednesday	15
Thursday	16
Friday	15
Total	77

Dr. K. Prashanth NARASIMHA  
Principal/Director

K S School of Engineering and Management  
Bengaluru - 560 109



**K S School of Engineering and Management, Bengaluru-109**  
**Department of Electronics and Communication Engineering**  
**CALENDAR OF EVENTS: ODD SEMESTER (2019-2020)**

**SESSION: JUL 2019 – NOV 2019**

Week No.	Month	Day						Days	Institutional Activities / Holidays	Departmental Activities
		Mon	Tue	Wed	Thu	Fri	Sat			
1	July/Aug	29	30	31	1	2	3DH	5	3 - Declared Holiday	
2	Aug	5	6	7	8	9DH	10	5	9 - Varamaha Lakshmi (Declared Holiday) 10 - Monday Time Table	
3	Aug	12H	13	14	15H	16	17	4	12 - Bakrid 15 - Independence Day 17 - Tuesday TimeTable	
4	Aug	19	20	21	22	23	24DH	5	24 - Declared Holiday	
5	Aug	26	27	28	29	30	31 TA	6	31 - Wednesday Time Table	26-28 - IoT Workshop for 5th and 7th Sem students
6	Sep	2H	3 T1	4 T1	5 T1	6	7DH	4	2 - Vinayaka Chathruthi 7 - Declared Holiday	
7	Sep	9	10H	11 BV	12 ASD	13 MA	14	5	10 - Moharam 14 - Friday Time Table	
8	Sep	16	17	18	19	20	21DH	5	16-20 First feedback 21 - Declared Holiday	17- Seminar 18 Guest Lecture 21 - Workshop on eSim
9	Sep	23	24	25	26	27	28H	5	28 - Mahalaya Amavasya	24 - Technical Talk
10	Sep/Oct	30	1	2H	3	4	5	5	2 - Gandhi Jayanthi 5-Ayudha Pooja in College 5 - Monday Time Table	
11	Oct	7H	8H	9	10	11	12 TA	4	7 - Ayudha Pooja 8 - Vijaya Dashami 12 - Monday Time Table	
12	Oct	14 T2	15 T2	16 T2	17	18	19DH	5	19 - Declared Holiday	18 - Technical Talk
13	Oct	21	22	23 BV	24 ASD	25 MA	26	6	26 - Tuesday Time Table	
14	Oct/Nov	28	29H	30	31	1H	2DH	3	29 - Balipadyami 1 - Kannada Rajyothava 2 - Declared Holiday	
15	Nov	4	5	6	7	8	9	6	9 - Tuesday Time Table 4-8 Second feedback	
16	Nov	11	12	13	14	15H	16DH	4	15 - Kanakadasa Jayanthi 16 - Declared Holiday	
17	Nov	18	19	20	21 LT	22 LT	23 LT	6		21 - Technical Talk
18	Nov	25 T3	26 T3	27T3	28	29 BV ASD	30* MA	6	30 - Thursday Time Table and Last Working Day	28 - Industrial Visit for 7th Sem Students
<b>Total No. of Working Days: 89</b>										

H	Holiday
BV	Blue Book Verification
T1,T2,T3	Tests 1,2,3
ASD	Attendance & Sessional Display
DH	Declared Holiday
LT	Lab Test
TA	Test Attendance
MA	Master Attendance Filling

Total Number of Working Days ( Excluding Holidays and Tests)

Monday	16
Tuesday	15
Wednesday	15
Thursday	16
Friday	15
Total	77

**Department Head**  
**Professor & Head**  
**Dept. of Electronics & Communication Engineering**  
**K. S. School of Engineering & Management**  
**Bangalore-560 109**

**Principal**  
**Dr. K. RAMA NARASIMHA**  
**Principal/Director**  
**K S School of Engineering and Management**  
**Bengaluru - 560 109**



**K. S. SCHOOL OF ENGINEERING AND MANAGEMENT - 560 109**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

**Session 2019-2020 (Odd semester: July-Nov2019)**

**First Internal Time Table**

Sl. No	Date	Day	Semester	Morning Session	Afternoon Session
				Time: 9:30am to 11:00am	Time: 1:30pm to 3:00pm
1	03/09/2019	Tuesday	III	Transform Calculus, Fourier Series and Numerical Techniques (18MAT31)	Network Theory (18EC32)
			V	Management and Entrepreneurship Development (17ES51)	Digital Signal Processing (17EC52)
			VII	Microwave and Antennas (15EC71)	Digital Image Processing (15EC72)
2	04/09/2019	Wednesday	III	Electronic Devices (18EC33)	Digital System Design (18EC34)
			V	Verilog HDL (17EC53)	Information Theory & Coding (17EC54)
			VII	Power Electronics (15EC73)	Real Time Systems (155EC743) / Multimedia Communication (15EC741)
3	05/09/2019	Thursday	III	Computer Organization & Architecture (18EC35)	Power Electronics & Instrumentation (18EC36)
			V	Operating System (17EC553)	Automotive Electronics (17EC56) / Object Oriented Programming Using C++ (17EC562)
			VII	Satellite Communication (1EC755)	

Signature of Test Coordinator

Signature of the HOD  
 Professor & Head  
 Dept. of Electronics & Communication Engineering  
 K. S. School of Engineering & Management  
 Bangalore-560 109

Signature of the Principal  
 Dr. K. RAMA NARASIMHA  
 K S School of Engineering and Management  
 Bengaluru - 560 109



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**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

**Session 2019-2020 (Odd semester: July-Nov2019)**

**Second Internal Time Table**

Sl. No	Date	Day	Semester	Morning Session	Afternoon Session
				Time: 9:30am to 11:00am	Time: 1:30pm to 3:00pm
1	24/10/2019	Thursday	III	Transform Calculus, Fourier Series and Numerical Techniques (18MAT31)	Network Theory (18EC32)
			V	Management and Entrepreneurship Development (17ES51)	Digital Signal Processing (17EC52)
			VII	Microwave and Antennas (15EC71)	Digital Image Processing (15EC72)
2	25/10/2019	Friday	III	Electronic Devices (18EC33)	Digital System Design (18EC34)
			V	Verilog HDL (17EC53)	Information Theory & Coding (17EC54)
			VII	Power Electronics (15EC73)	Real Time Systems (155EC743) / Multimedia Communication (15EC741)
3	26/10/2019	Saturday	III	Computer Organization & Architecture (18EC35)	Power Electronics & Instrumentation (18EC36)
			V	Operating System (17EC553)	Automotive Electronics (17EC56) / Object Oriented Programming Using C++ (17EC562)
			VII	Satellite Communication (1EC755)	

Signature of Test Coordinator

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 Dept. of Electronics & Communication Engineering  
 K. S. School of Engineering & Management  
 Bangalore-560 109

Signature of the Principal/Director  
 K S School of Engineering and Management  
 Bengaluru - 560 109



**K. S. SCHOOL OF ENGINEERING AND MANAGEMENT - 560 109**  
**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**

Session 2019-2020 (Odd semester: July-Nov2019)

**Third Internal Time Table**

Sl. No	Date	Day	Semester	Morning Session	Afternoon Session
				Time: 9:30am to 11:00am	Time: 1:30pm to 3:00pm
1	25/0911/2019	Monday	III	Transform Calculus, Fourier Series and Numerical Techniques (18MAT31)	Electronic Devices (18EC33)
			V	Information Theory & Coding (17EC54)	Verilog HDL (17EC53)
			VII	Real Time Systems (155EC743) / Multimedia Communication (15EC741)	Power Electronics (15EC73)
2	26/11/2019	Wednesday	III	Digital System Design (18EC34)	Computer Organization & Architecture (18EC35)
			V	Automotive Electronics (17EC56) / Object Oriented Programming Using C++ (17EC562)	Operating System (17EC553)
			VII	Satellite Communication (1EC755)	Digital Image Processing (15EC72)
3	27/11/2019	Thursday	III	Power Electronics & Instrumentation (18EC36)	Network Theory (18EC32)
			V	Digital Signal Processing (17EC52)	Management and Entrepreneurship Development (17ES51)
			VII	Microwave and Antennas (15EC71)	
4	28/11/2019	Thursday	III	Constitution of India, Professional Ethics and Cyber Law (18CPC39)	

*[Signature]*  
Signature of Test Coordinator

*[Signature]*  
Professor & Head  
Signature of the HOD  
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*[Signature]*  
Dr. K. RAMANARASIMHA  
Principal/Director  
Signature of the Principal  
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