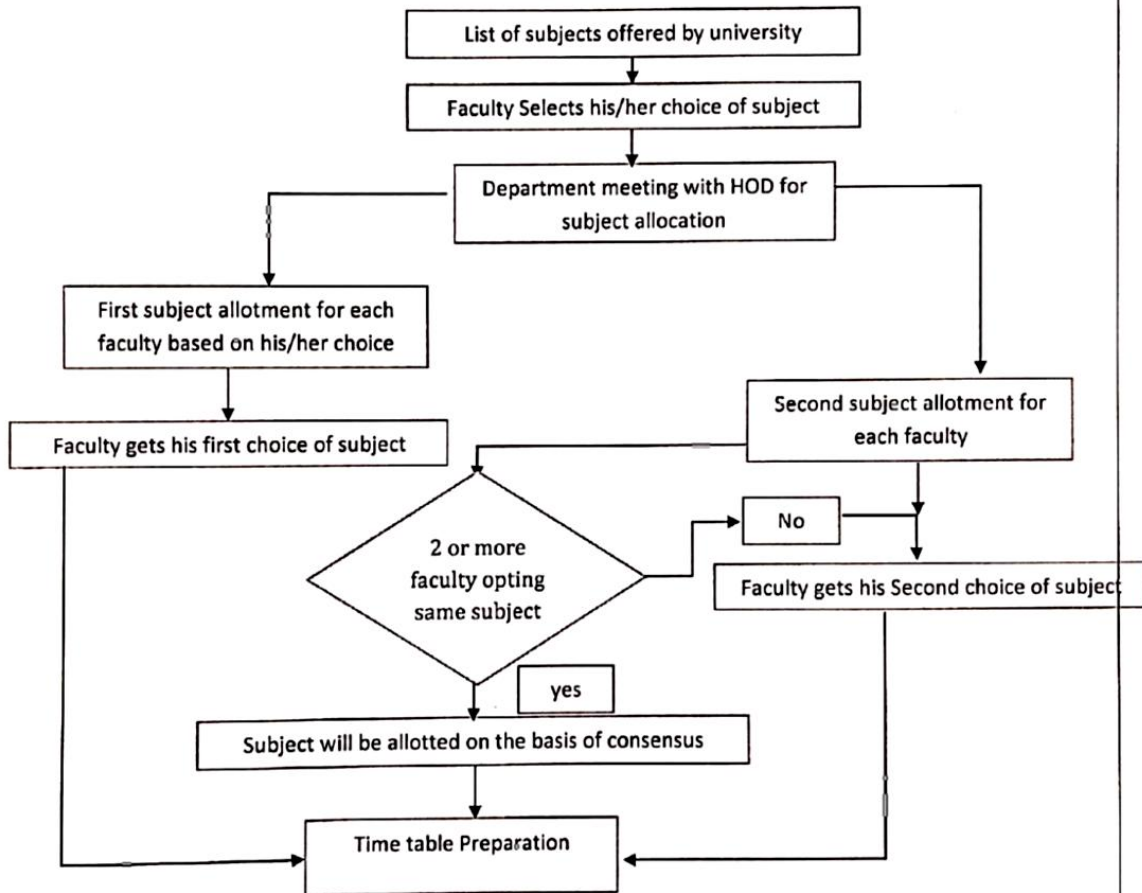




K.S. SCHOOL OF ENGINEERING AND MANAGEMENT

BANGALORE – 560109

SOP FOR THEORY SUBJECT ALLOTMENT



[Handwritten Signature]

Dr. K. RAMA NARASIMHA
Principal/Director
K S School of Engineering and Management
Bengaluru - 560 109



K. S. SCHOOL OF ENGINEERING AND MANAGEMENT- 560 109

**DEPARTMENT OF ELECTRONICS & COMMUNICATION
ENGINEERING**

Circular

Date: 16th May 2019

All the faculty members are hereby informed to provide the subject preferences for semester Aug-Nov 2019 in the attached sheet latest by 20th May 2019.

Syllabi of different schemes are attached for your information.

Head-Department

**Professor & Head
Dept. of Electronics & Communication Engineering
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**DEPARTMENT OF ELECTRONICS & COMMUNICATION
ENGINEERING**

Following are Syllabi of different Schemes

2018 SCHEME (I Sem)

CODE	SUBJECT
18ELN14	Basic Electronics

2018 SCHEME (III Sem)

CODE	SUBJECT
18MAT31	Transform Calculus, Fourier Series and Numerical Techniques
18EC32	Network Theory
18EC33	Electronic Devices
18EC34	Digital System Design
18EC35	Computer Organization & Architecture
18EC36	Power Electronics & Instrumentation
18ECL37	Electronic Devices & Instrumentation Laboratory
18ECL38	Digital System Design Laboratory
18KVK39/49 OR 18KAK39/49	Vyavaharika Kannada OR Aadalitha Kannada

2017 SCHEME (V Sem)

CODE	SUBJECT
17ES51	Management and Entrepreneurship Development
17EC52	Digital Signal Processing
17EC53	Verilog HDL
17EC54	Information Theory & Coding
17EC55X	Professional Elective-I (Operating System / MSP430 Microcontroller)
17EC56X	Open Elective-I (Automotive Electronics / Object Oriented Programming Using C++)
17ECL57	DSP Lab
17ECL58	HDL Lab


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2015 SCHEME (VII Sem)

CODE	SUBJECT
15EC71	Microwave and Antennas
15EC72	Digital Image Processing
15EC73	Power Electronics
15EC74X	Professional Elective-3 (Multimedia Communication / Real Time Systems)
15EC75X	Professional Elective-4 (Satellite Communication)
15ECL76	Advanced Communication Lab
15EC7L77	VLSI Lab
15ECP78	Project Work Phase-I + Project work Seminar


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DEPARTMENT OF ELECTRONICS & COMMUNICATION
ENGINEERING

Session 2019-2020 (Odd semester: Aug-Nov 2019)

Preferences for Subjects

Sl. No.	Name of the Faculty	I Semester 2018	III Semester 2018	V Semester 2017	VII Semester 2015	Signature
1	Dr. Girish V Attimarad		ED	ITC	Sat Comm	<i>hmv</i>
2	Dr. Karthik P		Network Theory	ITC, OS	DIP, RTS	<i>PP</i>
3	Dr. Dushyanth N D		Network Theory	DSP	DIP	<i>DD</i>
4	Mr. S M Prabhu		Network Theory	MNE		<i>M. Srin</i>
5	Mr. Sanjay B Nayak		DSD	oop c++	DIP	<i>su</i>
6	Mr. Senthil Babu		Network Theory	DSP	MMC	<i>S.P.</i>
7	Mr. Manu D K	B.E	DSD	ITC	P.E	<i>Manu</i>
8	Mrs. Shantala G M	B.E	DSD	Verilog HDL	P.E VLSI lab	<i>Shan</i>
9	Mr. Gopala Krishnamurthy C R	B.E	NT, AEC DE	AC Verilog HDL	PE	<i>Gopala</i>
10	Dr. Kishore M	BE	CO, PEI	ITC	PE	<i>Kishore</i>
11	Mr. Arun Kumar M		PEI EDI Lab	C++ DSP Lab	PE PE Lab	<i>Arun</i>
12	Mrs. Shalini Shravan		DSD, NT	ITC, Verilog HDL	M&A	<i>Shalini</i>
13	Mr. Ravikiran B A		DSDL	DSP DSPL	mmc	<i>BABU</i>
14	Mrs. Renuka V Tali	BE	DSD	HDL	MMC	<i>Renuka</i>
15	Mr. Puneeth S	BE	CO ED Lab	ITC DSP, AC DSP Lab	M&A, PE VLSI Lab	<i>Puneeth</i>
16	Mr. Vinay R		ED	ITC	PEIP	<i>Vinay R</i>
17	Mrs. Gunasheela T J	B.E	ED	ITC, MNE DSP Lab	Ac Lab M&A, PE	<i>Gunasheela</i>
18	Mr. Syed Waseem Tabraiz	B.E	DSD, ED, DSD Lab	ITC	Satcom	<i>Waseem</i>
19	Mrs. Shilpa A P	BE	DEC VLSI Lab	ITC	Ac Lab M&A, PE	<i>Shilpa</i>
20	Mrs. Deepa R Bhang		ED ED Lab	VHDL	VLSI Lab	<i>Deepa</i>
21	Mrs. Kripa	BE	Verilog HDL DSD	VHDL, ITC	Satellite communication	<i>Kripa</i>
22	Ms. Zaiba		DSD	Automotive Electronics	AC Lab	<i>Zaiba</i>

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DEPARTMENT OF ELECTRONICS & COMMUNICATION
ENGINEERING

Session 2019-2020 (Odd semester: Aug-Nov 2019)

SUBJECT ALLOCATION


BE I Semester (2018 Scheme)

Course Code	Course Title	Faculty		
18ELN14	Basic Electronics	SGM	SWT	SAP

BE III Semester (2018 Scheme)

Course Code	Course Title	Sec A	Sec B
18MAT31	Transform Calculus, Fourier Series and Numerical Techniques	Maths Faculty	Maths Faculty
18EC32	Network Theory	SS	SMP
18EC33	Electronic Devices	DRB	VR
18EC34	Digital System Design	SAP	RVT
18EC35	Computer Organization & Architecture	KM	PS
18EC36	Power Electronics & Instrumentation	CRG	CRG
18ECL37	Electronic Devices & Instrumentation Laboratory	CRG, VR, SWT, KB	CRG, VR, SWT, KB
18ECL38	Digital System Design Laboratory	SBN, SWT, ZB	SBN, SWT, ZB
18KVK39/49 OR 18KAK39/49	Vyavaharika Kannada OR Aadalitha Kannada	Prof. Trimurthy	


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BE V Semester (2017 Scheme)

Course Code	Course Title	Sec A	Sec B
17ES51	Management and Entrepreneurship Development	GT	KB
17EC52	Digital Signal Processing	SB	SB
17EC53	Verilog HDL	SGM	RVT
17EC54	Information Theory & Coding	MDK	MDK
17EC55X	Professional Elective-1 (Operating System/MSP 430 Microcontroller)	KP / ZB	KP / ZB
17EC56X	Open Elective-1 (Automotive Electronics / Object Oriented Programming Using C++)	AKM/SBN	AKM/SBN
17ECL57	DSP Lab	SB, DND, AKM, RBA	SB, DND, AKM, RBA
17ECL58	HDL Lab	KM, SS, RVT	KM, SS, RVT

BE VII Semester (2015 Scheme)

Course Code	Course Title	Sec A	Sec B
15EC71	Microwave and Antennas	GTJ	PS
15EC72	Digital Image Processing	DND	RBA
15EC73	Power Electronics	AKM	VR
15EC74X	Professional Elective-3 (Multimedia Communication / Real Time Systems)	RBA	DND
15EC75X	Professional Elective-4 (Satellite Communication)	KB	GVA
15ECL76	Advanced Communication Lab	MDK, SS, PS, GTJ	MDK, SS, PS, GTJ
15EC7L77	VLSI Lab	SGM, SAP, DRB, KB	SGM, SAP, DRB, KB
15ECP78	Project Work Phase-I + Project work Seminar	GVA, KP, SMP	GVA, KP, SMP

Lab In-Charge Faculty

Semester	Lab	Faculty
III SEM	Microcontroller Lab	CRG, VR, SWT, KB
	Analog Circuits Lab	SBN, SWT, ZB
V SEM	Embedded Controller Lab	SB, DND, AKM, RBA
	Computer Communication Networks Lab	KM, SS, RVT
VII SEM	Advanced Communication Lab	SGM, SAP, DRB, KB
	VLSI Lab	GVA, KP, SMP


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Subjects Allotment (Faculty-wise)

Sl. No.	Faculty	Subject- 1	Subject -2	Lab	Signature
1	Dr. Girish V Attimarad (GVA)	SC (VII B)		Project	
2	Dr. Karthik P (KP)	OS (VA&B)		Project	
3	Dr. Dushyanth N D (DND)	DIP (VIIA)	RTS (VIIB)	DSP	
4	Prof. S M Prabhu (SMP)	NT (IIIB)		Project	
5	Prof. Sanjay B Nayak (SBN)	C++ (VA&B)		DSD	
6	Prof. Senthil Babu (SB)	DSP (VA)	DSP (VB)	DSP	
7	Prof. Manu D K (MDK)	ITC (VA)	ITC (VB)	ADC	
8	Prof. Shantala G M (SGM)	VHDL (VA)	BE (I)	VLSI	
9	Prof. Gopala Krishnamurthy C R (CRG)	PEI (IIIA)	PEI (IIIB)	ED	
10	Dr. Kishore M (KM)	CO (IIIA)		HDL	
11	Dr. Arun Kumar M (AKM)	AE (VA&B)	PE (VIIA)	DSP	
12	Prof. Shalini Shravan (SS)	NT (IIIA)		HDL, ADC	
13	Prof. Ravikiran B A (RBA)	MMC (VIIA)	DIP (VIIB)	DSP	
14	Prof. Renuka V Tali (RVT)	DSD (IIIB)	VHDL (VB)	HDL	
15	Prof. Puneeth S (PS)	NT (IIIB)	M&A (VIIB)	ADC	
16	Prof. Vinay R (VR)	ED (IIIB)	PE (VIIB)	ED	
17	Prof. Gunasheela T J (GTJ)	M&E (VA)	M&E (VB)	ADC	
18	Prof. Syed Waseem Tabraiz (SWT)	BE		DSD, ED	
19	Prof. Shilpa A P (SAP)	DSD (IIIA)	BE	VLSI	
20	Prof. Deepa R Bhangi (DRB)	ED (IIIA)		ED, VLSI	
21	Prof. Kripa ^K B (KB)	M&E (VB)	SC (VIIA)	VLSI	
22	Prof. Zaiba (ZB)	MSP430 (VA&B)		DSD, ADC	

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DEPARTMENT OF ELECTRONICS & COMMUNICATION
ENGINEERING

Session 2019-2020 (Odd semester: Aug-Nov 2019)

PROCESS ALLOCATION

Sl. No.	Process	Faculty (Coordinator)	Signature
1	IQAC Department	RBA	BAGU
2	Affiliation Documentation	KP	AP
		RBA	BAGU
3	Time Table	SWT	F
		AKM	M.S.
4	Internal (Test)	CRG	Uppal
		SAP	P. J. G.
5	CIE	SS	A
6	Electives	CRG	Uppal
7	Result Analysis	GTJ	D
		KB	K
8	Project	GVA	h.m.
		KP	AP
		SMP	M. S.
9	Placement	PS	P. J. G.
10	Internship	MDK	h.m.
11	Alumni Association	SBN	h.m.
		VR	Uppal R
		ZA	Zaib
12	Website	DRB	A
13	Department Library	SWT	F
14	Newsletter	SBN	h.m.
		DRB	A
15	Forum (Exceltron)	MDK	h.m.
16	R & D	KP	AP
		SMP	M. S.
17	Publication	GTJ	D
18	Professional Activities	GVA	h.m.
		SMP	D
19	FDP (conducted/ attended)	GVA	h.m.
		KP	AP
		SMP	M. S.
20	Workshop /Seminar (conducted/ attended)	GVA	h.m.
		KP	AP
		SMP	M. S.
21	Pupilpod	VR	Uppal R
22	Proctor	KM	h.m.
23	CCM	PS	P. J. G.


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24	PTM	KM	<i>Aswari</i>
25	Budget	AKM	<i>Madh...</i>
26	Industry Visit	MDK	<i>leau</i>

Class Teachers

Semester	Section	Faculty	Signature
I SEM	E	SWT	<i>[Signature]</i>
	F	SAP	<i>[Signature]</i>
	G	SGM	<i>[Signature]</i>
III SEM	A	SS	<i>[Signature]</i>
	B	PS	<i>[Signature]</i>
V SEM	A	SB	<i>[Signature]</i>
	B	ZB	<i>[Signature]</i>
VII SEM	A	DND	<i>[Signature]</i>
	B	VR	<i>[Signature]</i>

NAAC Coordinators

Sl. No.	NAAC Criteria	Faculty (Coordinators)	Signature
1	Criteria-1	RBA, VR, KB	<i>[Signatures]</i>
2	Criteria-2	SB, AKM, SS, PS, DRB	<i>[Signatures]</i>
3	Criteria-3	GVA, DND, RVT, GTJ	<i>[Signatures]</i>
4	Criteria-4	MDK, SWT	<i>[Signatures]</i>
5	Criteria-5	SBN, CRG, ZB	<i>[Signatures]</i>
6	Criteria-6	SGM, KM, SAP	<i>[Signatures]</i>
7	Criteria-7	SGM, KM, SAP	<i>[Signatures]</i>

[Signature]
Head - Department
Professor & Head
 Dept. of Electronics & Communication Engineering
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[Signature]
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DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

Session 2019-2020 (Odd semester: Aug-Nov 2019)

Faculty Workload

Sl. No.	Faculty	Designation	Subject-1	Subject-2	Laboratory	Processes/Activities	Workload (Hours)				
							Theory	Lab	Project	Seminar	Total
1	Dr. Girish V Attimarad (GVA)	Professor	SC (VII B)		Project	Head of Dept., Course File, Personal File, Project Proposals and FDP Proposals, IEEE Student Activities, Overall Department NAAC Documentations, Purchase Committee, Anti Sexual Harassment Committee, Appointment Committee, NAAC & Department Progress Committee, Project Co-ordinator, Professional Activities, Mentor	4	0	2	2	8.0
2	Dr. Karthik P (KP)	Professor	OS (VA&B)		Project	Technical Bodies IETE / CSI / SAE (Chairperson), Students Project / KSCST / VGST Committee, Course File, Personal File, Project Proposals and FDP Proposals, AICTE affiliation, LIC affiliation, Research Center Affiliation, Project Co-ordinator, R & D Activities Co-ordinator, Mentor	4	0	3	1	8.0
3	Dr. Dushyanth N D (DND)	Professor	DIP (VIA)	RTS (VIIB)	DSP	Class Teacher for VII A, EDC / IIP / IPR Committee (Chairperson), Time Table Co-ordinator (Dept. & VTU Lab Exams), Lab In-Charge of DSP Lab, Project Proposals and FDP Proposals, NAAC Criteria 3 incharge, Mentor.	8	4.5	2	1	15.0
4	Prof. S M Prabhu (SMP)	Assoc. Professor	NT (IIIB)		Project	Project Co-ordinator, R & D Activities Co-ordinator, Professional Activities, Project Proposal Preparation for the Department, IEEE Student Chapter Activity, Mentor.	4	0.0	2	1	7.0
5	Prof. Sanjay B Nayak (SBN)	Assoc. Professor	C++ (VA&B)		DSD	Seminar and Workshop Co-ordinator, Monthly News Letter Co-ordinator, Alumni Committee (Chairperson), Alumni Association Co-ordinator, Guest Lecture Supporting files to be maintained, NAAC Criteria-5 Co-ordinator, Mentor	4	7.5	2	1	14.5
6	Prof. Senthil Babu (SB)	Assoc. Professor	DSP (VA)	DSP (VB)	DSP	Class Teacher for V A, Disciplinary & Anti Ragging Committee, Lab In-Charge of DSP Lab, NAAC Criteria-2 Co-ordinator, Mentor.	8	4.5	4	1	17.5
7	Prof. Manu D K (MDK)	Assoc. Professor	ITC (VA)	ITC (VB)	ADC	Class Teacher VI A, Internship Co-ordinator, Forum (Exceltron) Co-ordinator, Lab Incharge for ADC Lab, Sports Committee, Industry Visit, NAAC Criteria-4 Co-ordinator, Quotation File, PO & Invoice, FDP/Industrial Visit, Industry Institute Interaction files to be maintained, Mentor	9	4.5	2	1	16.5
8	Prof. Shantala G M (SGM)	Assoc. Professor	VHDL (VA)	BE (I)	VLSI	Class Teacher for I G, Lab Incharge for VLSI Lab, Grievances Committee/ Human Rights Club, NAAC Criteria-6 & 7 Co-ordinator. Circular Files (Office & VTU) files to be maintained, Mentor.	8	4.5	2	1	15.5
9	Prof. Gopala Krishnamurthy C R (CRG)	Assoc. Professor	PEI (IIIA)	PEI (IIIB)	ED	Hostel & Canteen Committee, Publicity & Information Committee (Chairperson), Internal (Test) Co-ordinator, Electives Co-ordinator, Lab In-Charge of ED Lab, Equipment File (Software & Hardware), NAAC Criteria-5 Co-ordinator, Mentor.	8	4.5	2	1	15.5
10	Dr. Kishore M (KM)	Asst. Professor	CO (IIIA)		HDL	PTM & Proctor Incharge, NAAC Criteria-6 & 7 Co-ordinator, Stock Book Verification, NAAC files to be maintained for the above mentioned documents, Mentor	8	4.5	2	1	15.5
11	Dr. Arun Kumar M (AKM)	Asst. Professor	AE (VA&B)	PE (VIA)	DSP	Time Table Co-ordinator (Class and VTU Practical Schedules), Seminar Co-ordinator, Transport Committee, NAAC Criteria-2 Co-ordinator, Budget File, NAAC files to be maintained, Mentor	9	4.5	2	1	16.5

12	Prof. Shalini Shravan (SS)	Asst. Professor	NT (IIIA)		HDL, ADC	Class Teacher for III A, Hostel & Canteen Committee, Academic Committee, CIE Co-ordinator, NAAC Criteria-2 Co-ordinator, Lab In-Charge of HDL Lab, IA Mark Entry VTU as well Internal Test, NAAC Criteria 2 files to be maintained, Mentor.	4	7.5	2	1	14.5
13	Prof. Ravikiran B A (RBA)	Asst. Professor	MMC (VIIA)	DIP (VIIB)	DSP	Students Project / KSCST / VGST Committee, Newsletter & Magazine Committee (Chairperson), Affiliation Documentation, Pupilpod Co-ordinator (College), NAAC Criteria-1 Co-ordinator, AICTE, AISHE, NIRF files to be maintained, Mentor.	8	4.5	4	1	17.5
14	Prof. Renuka V Tali (RVT)	Asst. Professor	DSD (IIIB)	VHDL (VB)	HDL	Lab Incharge for HDL Lab, NAAC Criteria-3 Co-ordinator, Cultural Committee, Class Teacher 6th Sem B, Calender of Events files to be maintained, Mentor.	8	4.5	2	1	13.5
15	Prof. Puneeth S (PS)	Asst. Professor	CO (IIIB)	M&A (VIIB)	ADC	Class Teacher for III B, Placement Co-ordinator, Souvenir Incharge, Training & Placement Cell Committee, NAAC Criteria-2 Co-ordinator files to be maintained, Mentor.	9	4.5	2	1	16.5
16	Prof. Vinay R (VR)	Asst. Professor	ED (IIIB)	PE (VIIB)	ED	Class Teacher for VII B, Pupilpod Co-ordinator, NAAC Criteria-1 Co-ordinator, Alumni Co-ordinator, Department minutes of Meeting file, NAAC Criteria files to be maintained, Mentor.	9	4.5	2	1	16.5
17	Prof. Gunasheela T J (GTJ)	Asst. Professor	M&E (VA)	M&E (VB)	ADC	Lab Incharge of ADC Lab, Result Analysis Co-ordinator, Publication of ECE Dept., NSS Committee, NAAC Criteria-3 Co-ordinator files to be maintained, Mentor.	9	4.5	2	1	16.5
18	Prof. Syed Waseem Tabraiz (SWT)	Asst. Professor	BE		DSD, ED	Time Table Co-ordinator (Dept. & VTU Lab Exams), Class Teacher I E, Department Library Co-ordinator, Lab Incharge of DSD Lab, Library Committee, NAAC Criteria-4 Co-ordinator files to be maintained, Mentor.	5	4.5	2	1	12.5
19	Prof. Shilpa A P (SAP)	Asst. Professor	DSD (IIIA)	BE	VLSI	Class Teacher for I F, Stock Verification Committee, Lab In-Charge of DSD Lab, Internal (Test) Co-ordinator, NAAC Criteria-6 Co-ordinator, Dept. Activity file, Stock Book Verification, NAAC Criteria files to be maintained, Mentor.	9	4.5	2	1	16.5
20	Prof. Deepa R Bhangi (DRB)	Asst. Professor	ED (IIIA)		ED, VLSI	Placement Co-ordinator, Lab Incharge for ED Lab, Website & Newsletter Co-ordinator, NAAC Criteria-2 Co-ordinator, Research Center files files to be maintained, Mentor.	4	9.0	2	1	16.0
21	Prof. Kripa B (KB)	Asst. Professor	M&E (VB)	SC (VIIA)	VLSI	Lab In-Charge of VLSI Lab, Result Analysis CO-ordinator, Cultural Committee, NAAC Criteria-1 Co-ordinator files to be maintained, Mentor.	4	9.0	2	1	16.0
22	Prof. Zaiba (ZB)	Asst. Professor	MSP430 (VA&B)		DSD, ADC	Class Teacher for VB, Lab Incharge of DSD Lab, Alumni Association Co-ordinator, Industrial Visit Committee, Identified Laboratory, Identified Class Rooms, Expert Panel File, NAAC Criteria-5 Co-ordinator files to be maintained, Mentor.	4	7.5	2	1	14.5

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